Minimize Adverse Motor and Adjustable Speed Drive Interactions

Electronic adjustable speed drives (ASDs) are extremely efficient and valuable assets to motor systems. They allow precise process control and provide energy savings within systems that do not need to operate continuously at full output.

The most common ASD design sold today is the pulse-width-modulated (PWM) variable frequency drive (VFD) with a fast-rise-time insulated gate bipolar transistor (IGBT) to reduce switching losses and noise levels. However, higher carrier frequencies and faster rise-time transistors on PWM VFDs can produce voltage spikes or overshoots that can stress motor windings and bearings. Fortunately, these problems can be eliminated through proper design and equipment selection.

VFD Characteristics

All VFDs rectify the 60-hertz (Hz) fixed-voltage alternating current (AC) to direct current (DC) and use an inverter to simulate an adjustable frequency and variable-voltage AC output. Transistors, or electronic “switches,” create the AC voltage output but have very high losses when they create wave shapes other than square waves.

To minimize switching losses and approximate sine waves, VFDs operate these switches full-on or full-off, creating square waves of much higher frequency than the fundamental frequency, usually between 2 kilocycles per second (kHz) and 20 kHz. This is called a carrier wave, as shown in Figure 1. Each on-portion of the carrier wave is called a pulse, and the duration of on-time of each pulse is called the pulse width. The pulses do not turn on instantaneously; there is a brief rise time.

Different types of transistors used in drives have different rise times. Voltage spikes originate with fast rise time, and carrier frequencies above 5 kHz are likely to cause bearing damage unless protective measures are taken.

Design Considerations

Several design considerations should be taken into account when purchasing a VFD or fixing problems with an existing one. On new installations, ensure that no harm comes to motors by minimizing the cable length from the VFD to the motor. VFDs can produce voltage overshoots or spikes with the increase over the normal peak voltage dependent upon both cable length and rise time.
In Figure 2, the voltage increase is plotted against rise time in microseconds. Rise time is the time required for the voltage to increase from 10% to 90% of its steady-state value. The rise time is a characteristic of the power transistor switches and can be provided by the drive supplier. Modern IGBT switches operate well down toward the left side of the graph, so cable lengths of 50 feet or more almost always need mitigation.

Longer cables reflect the voltage rise so that the reflections reinforce the original pulse rise. This produces electrical resonance or “ringing” characterized by an oscillating voltage overshoot. With shorter cables, rapid rise time is not a problem. Review Figure 2 to determine voltage increase for a given rise time and cable length, and compare the result with the maximum values described below.

Existing general-purpose low-voltage motors may work fine with PWM VFDs if peak voltages due to ringing are held below 1,000 volts. If high-frequency voltage overshoots exceed 1,000 volts, electrical stresses can cause a turn-to-turn short within a motor coil group, usually within the first couple of turns.

Voltage overshoot is best avoided by locating the drive close to the motor. If a short cable run is not possible, a filtering device must be used. Sometimes VFD manufacturers provide a filter device with the drive or even in the drive cabinet. There are two common filter arrangements: the first uses line inductors (sometimes called load reactors), which should be placed at the drive end of the cable; and the second uses dV/dt filters. A tuned resistor-inductor filter increases the voltage rise time and effectively reduces dV/dt. There are some losses associated with the filters, so keeping cables short remains the best option.

The fast rise time pulses from a PWM VFD can also create a potentially harmful current flow in bearings even when overvoltage is not significant. Causes include common mode voltage problems and/or phase voltage unbalance on the VFD input. Capacitive coupling, resulting from irregular current waveforms and ground-mode currents, can damage bearings due to formation of a high-frequency voltage potential on the motor shaft that results in current flow across the bearing rolling elements. Problems also can occur in driven-load bearings if insulated couplings are not used. Reduce these problems by:

- Reducing the carrier frequency of the VFD to between 2 kHz and 5 kHz
- Using a dedicated, low-impedance ground wire from the motor frame to the case drives
- Selecting a suitable cable (i.e., symmetrical with all three phases equally spaced around the ground conductor)
- Selecting and matching cable impedance for the VFD.

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**Resources**

National Electrical Manufacturers Association (NEMA)—Visit [www.nema.org](http://www.nema.org) for information on motor standards, application guides, and technical papers.

U.S. Department of Energy (DOE)—For more information on motor and motor-driven system efficiency and to download the MotorMaster+ software tool, visit the Advanced Manufacturing Office (AMO) website at [manufacturing.energy.gov](http://manufacturing.energy.gov).