



Quadrennial Technology Review 2015

## Chapter 6: Innovating Clean Energy Technologies in Advanced Manufacturing

# Technology Assessments



*Additive Manufacturing*

*Advanced Materials Manufacturing*

*Advanced Sensors, Controls,  
Platforms and Modeling for  
Manufacturing*

*Combined Heat and Power Systems*

*Composite Materials*

*Critical Materials*

*Direct Thermal Energy Conversion  
Materials, Devices, and Systems*

*Materials for Harsh Service Conditions*

*Process Heating*

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*Roll-to-Roll Processing*

*Sustainable Manufacturing - Flow of  
Materials through Industry*

*Waste Heat Recovery Systems*

***Wide Bandgap Semiconductors for  
Power Electronics***



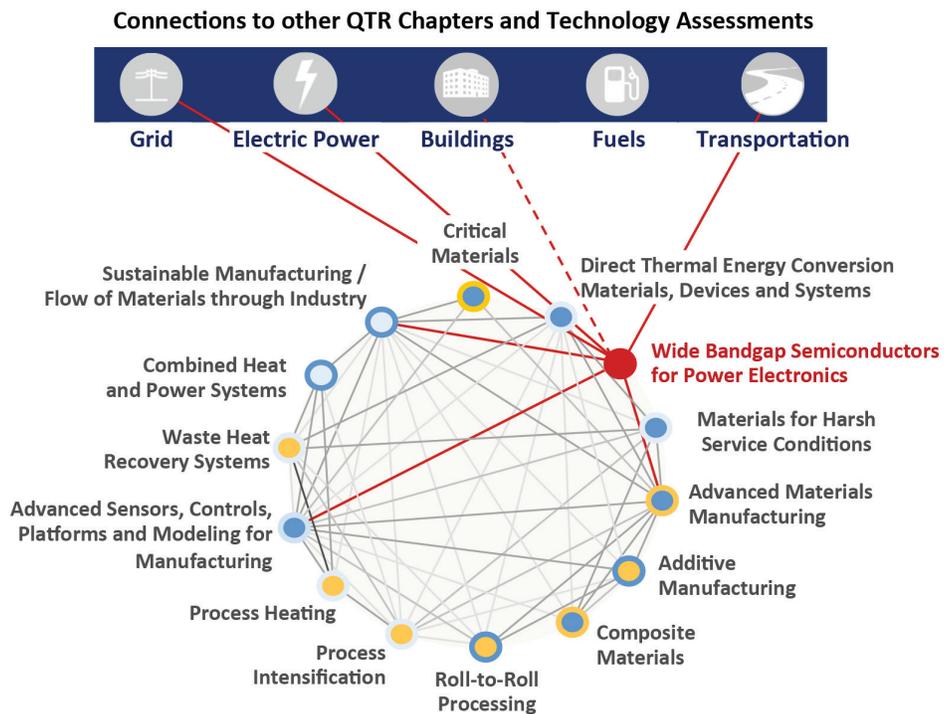
U.S. DEPARTMENT OF  
**ENERGY**



# Wide Bandgap Semiconductors for Power Electronics

## Chapter 6: Technology Assessments

*NOTE: This technology assessment is available as an appendix to the 2015 Quadrennial Technology Review (QTR). **Wide Bandgap Semiconductors for Power Electronics** is one of fourteen manufacturing-focused technology assessments prepared in support of Chapter 6: Innovating Clean Energy Technologies in Advanced Manufacturing. For context within the 2015 QTR, key connections between this technology assessment, other QTR technology chapters, and other Chapter 6 technology assessments are illustrated below.*



Representative Intra-Chapter Connections	Representative Extra-Chapter Connections
<ul style="list-style-type: none"> <li>■ <b>Sustainable Manufacturing:</b> smaller-footprint electronics with reduced cooling requirements</li> <li>■ <b>Advanced Sensors, Controls, Platforms, and Modeling for Manufacturing:</b> variable frequency drives and motor speed control</li> <li>■ <b>Advanced Materials Manufacturing:</b> low-cost, commercial-scale production methods for wide bandgap devices</li> </ul>	<ul style="list-style-type: none"> <li>■ <b>Grid:</b> solid state transformers for power flow control; inverters for renewable energy</li> <li>■ <b>Electric Power:</b> inverters for photovoltaic and wind power systems</li> <li>■ <b>Buildings:</b> variable speed drives for HVAC systems; AC-to-DC and DC-to-AC adapters</li> <li>■ <b>Transportation:</b> Power electronics for electric vehicles</li> </ul>

## Introduction to the Technology/System

The field of power electronics focuses on the use of solid-state electronic devices for the conversion, control, and processing of electricity and electric power. Silicon (Si) semiconductors have traditionally been employed in fabricating these power circuits. However, as devices and equipment evolve and become more powerful, existing Si power electronics are losing the ability to keep pace with increasing performance demands. This is primarily attributed to the fact that traditional Si device technology has matured to a point where it is pushing the fundamental limitations of this material system.

Wide bandgap (WBG) semiconductors have shown the capability to meet the higher performance demands of the evolving power equipment, operating at higher voltages and temperatures and enabling switching frequencies with greater efficiencies compared with existing Si devices. Along with performance improvements, WBG-based power electronics can be fabricated with a much smaller footprint (reduced volume) compared with comparable Si devices due to decreased cooling requirements and smaller passive components, contributing to overall lower system costs.

Reducing energy consumption is critical to U.S. economic, environmental, and security interests. In 2014, U.S. industrial plants accounted for 955 billion kWh of electricity consumption—over a quarter of end-use electrical consumption in the United States.<sup>1,2</sup> It is projected that this consumption will grow by 26% to 1270 billion kWh between 2014 and 2040.<sup>3</sup> Motors and generators are critical in industrial applications, driving equipment such as fans, pumps, compressors, and conveyer systems. To that end, increasing the efficiency of power electronics is imperative. The use of WBG semiconductors in variable frequency drives (VFDs) controlling these machines can result in significant levels of energy reduction as well as enabling substantial decreases in the weight and volume of the drive electronics. For example, one estimate stated that the heat sink size for the variable speed drive of a 10 horsepower (hp) industrial electric motor could be reduced by 66% if WBG-based power electronics were used.<sup>4</sup> Other applications where WBG power electronics could achieve appreciable energy savings include hybrid and electric vehicles, lighting, data servers, AC adapters, solar inverters, power supplies, charging circuits, and grid control.<sup>5</sup> Energy savings opportunities are quantified for many of these applications in the *Case Studies* at the end of this technology assessment.

Two major WBG materials with the potential to enable significant advances in power electronics are silicon carbide (SiC) and gallium nitride (GaN). To date, the predominant use of SiC in electronics is as a substrate for GaN light emitting diodes (LEDs). While SiC currently has limited use in power electronics, its role is expected to grow as it becomes the prevailing WBG replacement for Si in applications requiring device ratings in excess of 600 volts.<sup>6</sup> A major challenge to widespread adoption of SiC power electronics devices is the high cost of substrates and epitaxial materials. These high costs are tied to small production volumes and high manufacturing costs. Significant markets are expected for SiC devices in hybrid and electric vehicles as well as solar inverters and power supplies. SiC diodes are already used with companion Si transistors in photovoltaic (PV) inverters and hybrid vehicle chargers. Their greatest revenue-generating applications are expected to be in industrial motor drives<sup>7</sup> and hybrid and electric vehicles.<sup>8</sup>

GaN is currently widely used in LEDs and radio frequency (RF) amplifiers, and its emergence in power electronics is relatively recent.<sup>7</sup> Challenges for GaN-on-Si semiconductors, the current most cost-effective method for fabricating GaN power devices, are mostly related to their lack of maturity. Issues include overcoming material challenges, such as the high lattice strain at the GaN and Si interface owing to mismatches in the coefficient of thermal expansion. GaN high-electron-mobility transistors (HEMTs) are expected to be the dominant WBG semiconductor replacement for Si in applications requiring device ratings less than 600 volts,<sup>6</sup> whereas impacts have already been realized in RF and power-supply applications.<sup>7</sup>



There is much discussion about how the high frequency capabilities of WBG semiconductors might benefit imaging and sensing devices<sup>9</sup> or how smaller WBG LEDs might improve minimally invasive surgical probes, among other applications.<sup>10</sup> For example, magnetic resonance imaging (MRI) equipment generates significant noise because the gradient coil vibrates due to the Lorentz force. GaN-based devices could enable MRI systems to operate at higher frequencies, thereby reducing noise. These innovations are also targeted to save space by integrating the control equipment, which is currently placed in a separate room, into the MRI system.

The size reductions made possible by WBG devices could also enable a wider array of implantable medical devices. For cutting and coagulation of tissue in electrosurgical units, GaN-based devices can be used at higher frequencies of 500,000 to 3,000,000 Hz.<sup>11</sup> This is important to minimize nerve and muscle stimulation, which occurs at electrical frequencies below 10,000 Hz.<sup>12</sup> In addition, an exciting area has been in devices that power themselves through energy harvesting, such as self-powering pacemakers.<sup>13</sup> The improved efficiencies that WBG power electronics allow could mean that devices with even higher power requirements could eventually be made to last longer and be more compact by eliminating the need for batteries.

Currently, the United States is among the leading countries developing WBG technologies. Retaining and strengthening the WBG industry have been deemed priorities by the U.S. government for energy savings, economic development, and national security reasons.<sup>14, 15</sup> As such, there is a great deal of momentum behind public/private partnerships in this space. One major federal investment is in the Next Generation Power Electronics National Network for Manufacturing Innovation Institute (PowerAmerica), led by North Carolina State University.<sup>16, 17</sup> PowerAmerica was announced in January 2014, with a stated goal of making WBG power electronics cost-competitive within five years. Previously, progress had been made over the past 20 years in the material quality of SiC substrates, SiC epitaxy, and GaN/SiC and GaN/Si epitaxy with Department of Defense funding. PowerAmerica is, therefore, focusing its activities on device manufacturing, WBG-specific power module development, and electronics to leverage the attributes of WBG devices rather than on fundamental materials work. Individual states are also showing growing interest in investing in this area of research. For example, New York is supporting the New York Power Electronics Manufacturing Consortium (NY-PEMC), which is a partnership of over 100 private companies, including General Electric (GE) and IBM.

Going forward, the most important function of public/private partnerships will be to establish a technology and business development ecosystem for continued advancement of the WBG power electronics industry.

## Technology Assessment and Potential

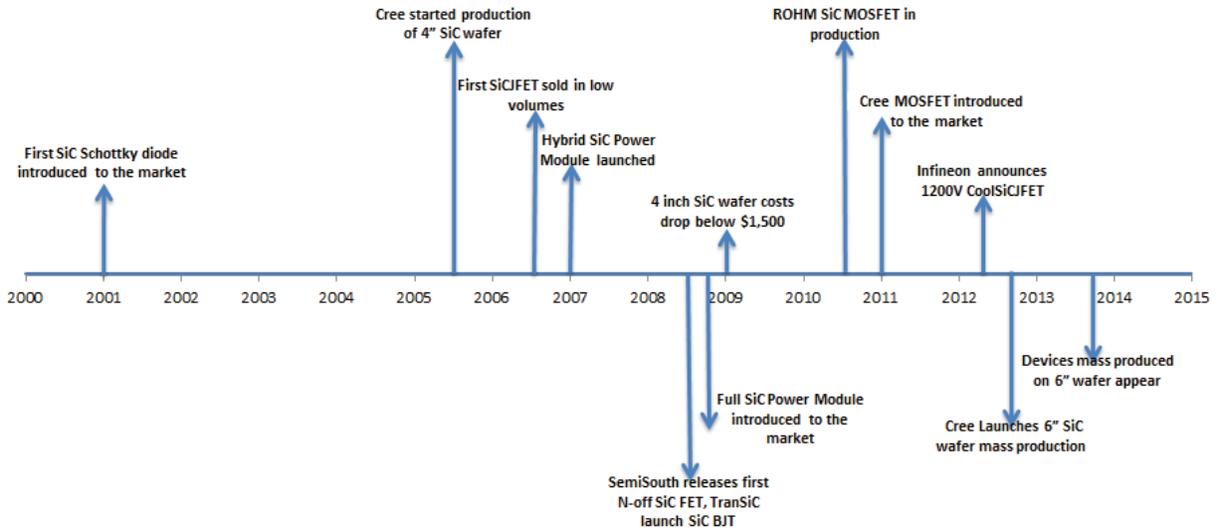
### Performance Advances in SiC

SiC power semiconductors are a relatively new entrant in the commercial marketplace, with the first SiC Schottky diode introduced in 2001.<sup>7</sup> This milestone and others in the history of SiC power electronics are noted in Figure 6.N.1. Despite the relatively recent emergence of SiC power devices, significant advances have been made. Six-inch SiC wafers are currently in production—they were first announced by Cree, Inc., in August 2012<sup>18</sup>—though they are not yet commonly used to produce power electronics devices. Wafer quality has also improved. The most common defects in SiC wafers have historically been “micropipes,” which are crystallographic defects resulting from the propagation of screw dislocations through epitaxial layers during wafer growth. The densities for these defects were in the range of 5–10/cm<sup>2</sup> in 2006,<sup>19</sup> improving to 0.75/cm<sup>2</sup> in 2014.<sup>20</sup> Japanese manufacturer Showa Denko has claimed even lower defect densities of 0.25/cm<sup>2</sup> in their six-inch wafers, announced in September 2014.<sup>21</sup>



**Figure 6.N.1** Milestones in SiC Power Electronics Development<sup>7</sup>

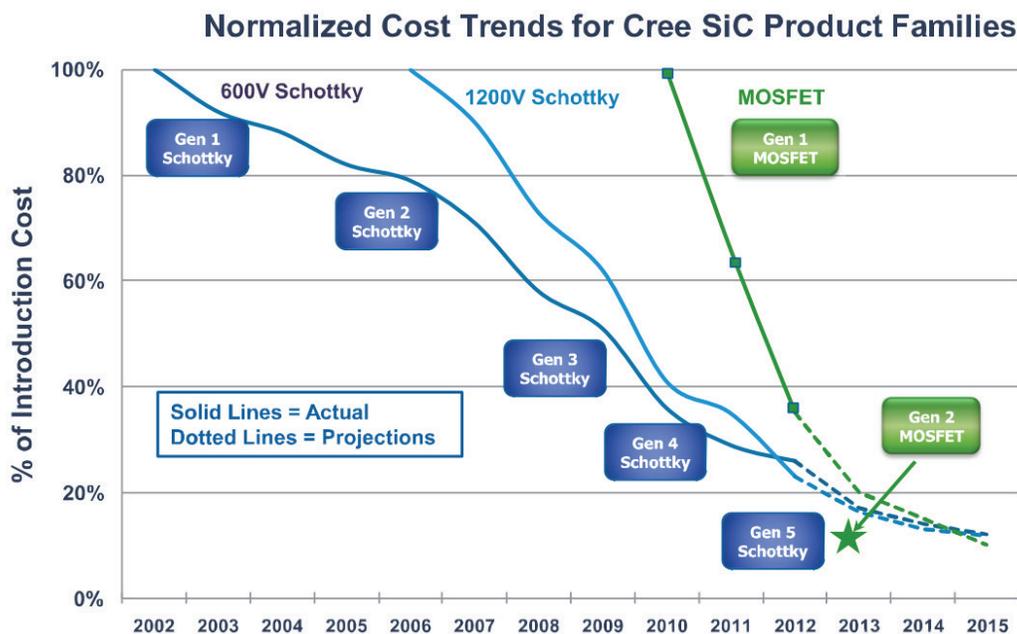
Credit: IHS Technology (<http://technology.ihs.com>): *The World Market for Silicon Carbide & Gallium Nitride Power Semiconductors* - 2013 Edition



In part because of improvements in wafer fabrication and production volume increases, device costs have declined dramatically since the first SiC Schottky diode was produced from a \$5,000, two-inch wafer. Four-inch SiC wafers have decreased in price from \$1,200–\$1,400 in 2009 to \$600–\$750 in 2012.<sup>4</sup> This time span also saw SiC power device sales more than triple.<sup>8</sup> Figure 6.N.2 shows the impact of these changes on the price of Cree, Inc., devices, from their introduction through 2012.<sup>4</sup>

**Figure 6.N.2** Decline of Device Cost for Cree, Inc., SiC Products Over Time<sup>4</sup>

Credit: Cree, Inc.

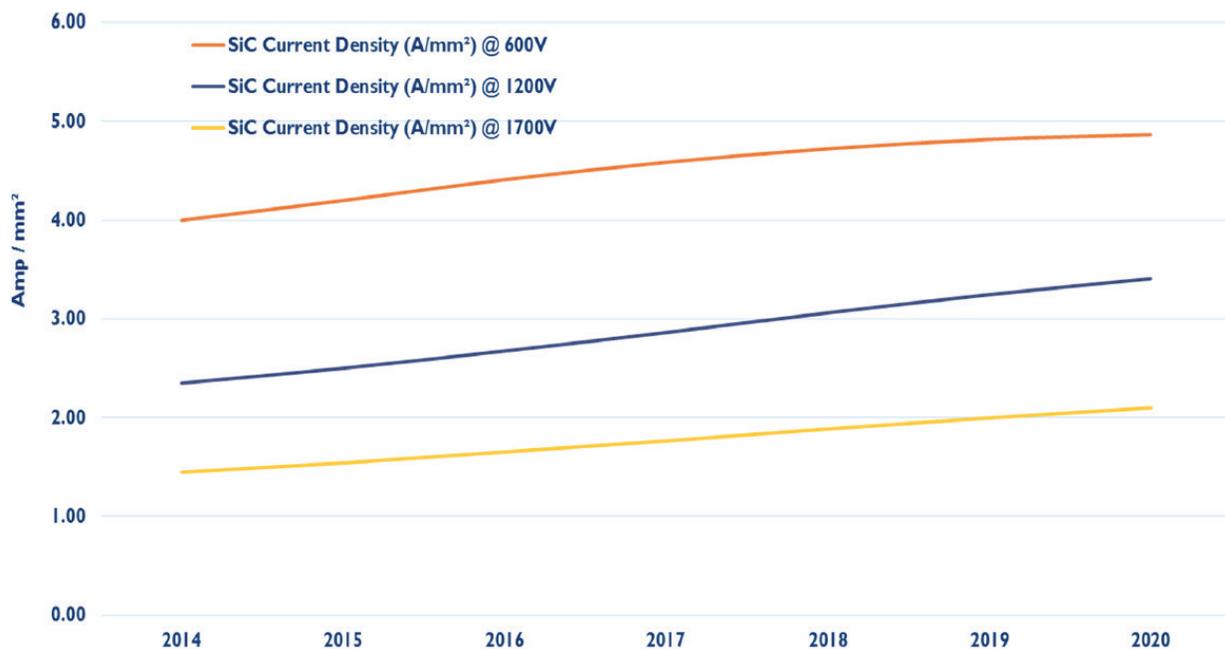




SiC device performance has also improved: in 2010, a 1200-Volt SiC device from Cree had an average current density of 1.2 A/mm<sup>2</sup>; <sup>22</sup> in 2014 this had risen to 2.3 A/mm<sup>2</sup>. <sup>23</sup> Growth projections for SiC devices are shown in Figure 6.N.3. A steady increase in current density is expected to continue through 2020 for high voltage devices, while current density growth in low voltage devices is projected to slow in the latter part of the decade. Schottky diodes are currently available with current ratings up to 50 amps, while 25 amps was the highest available in 2005.<sup>24</sup> Also, pronounced improvements in performance have occurred between subsequent generations of SiC power metal-oxide-semiconductor field-effect transistors (MOSFETs). For example, energy switching losses for 1,200V MOSFETs decreased by 28% from 0.78 mJ to 0.56 mJ during the 2011–2013 time frame.<sup>4</sup> Such performance improvements have been made possible through material developments.<sup>25</sup>

**Figure 6.N.3** Forecast of SiC Device Average Current Density Through 2020<sup>26</sup>

Credit: Yole Developpement



As SiC power electronics mature, technological advances will increasingly be driven by companies within the value chain, and device manufacturers will be key players. Table 6.N.1 lists the leading SiC power electronics device companies in terms of 2010 revenues.<sup>8</sup> The \$53 million SiC power electronics market in 2010 was led by two companies—Germany-headquartered Infineon (51% market share) and U.S. headquartered Cree, Inc. (37% share).<sup>8</sup> Both companies’ SiC fabrication facilities are located in the developed world—Infineon in Villach, Austria; and Cree, Inc., in Durham, North Carolina. In 2010, SiC power electronics were manufactured primarily in Europe (54%), the United States (41%), and Japan (2%). The distribution of market share is expected to change radically by 2020, at which time Japan is expected to have a 35% market share as a result of heavy industry and government investment.<sup>27</sup> Toyota has recently announced the beginning of on-road testing of SiC-power semiconductors in a Camry hybrid prototype and a fuel cell bus.<sup>28</sup> These tests will evaluate the performance of SiC technology, which could lead to significant efficiency improvements in hybrids and other electric-drive vehicles.

**Table 6.N.1** Distribution of 2010 SiC Power Electronics Device Revenues by Company and Fabrication Location<sup>8</sup>

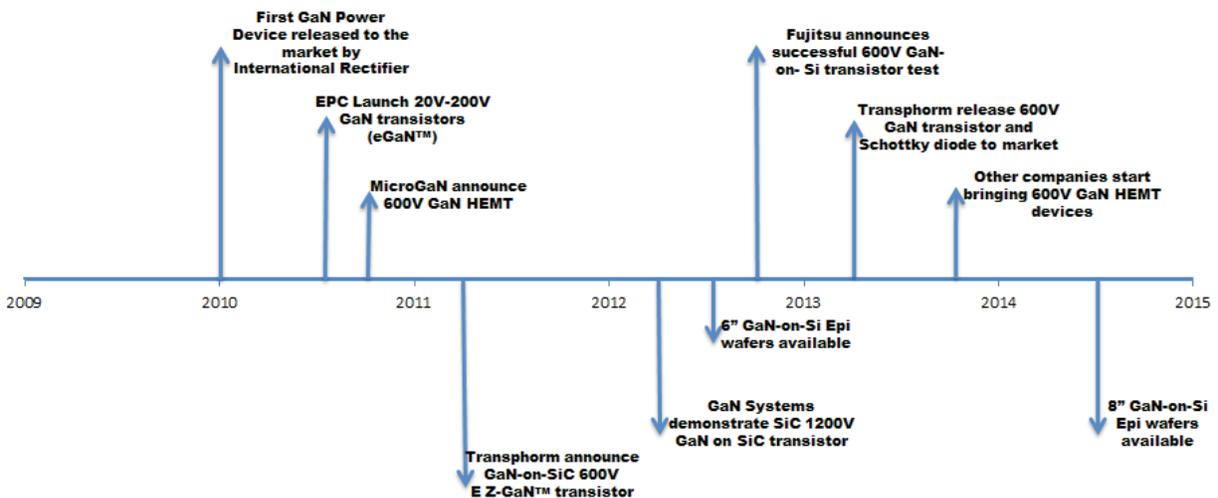
Company	2010 SiC Power Electronics Revenue (\$, millions)	Headquarters	Fabrication Location
Infineon	\$27.1	Germany	Villach, Austria
Cree, Inc.	\$19.7	U.S.	Durham, NC, U.S.A
STMicroelectronics	\$1.6	Switzerland	Catania, Italy
ROHM	\$1.1	Japan	Fukuoka, Japan and Miyazaki, Japan
All others	\$3.7		
<b>Total</b>	<b>\$53.2</b>		

## Performance Advances in GaN

GaN power electronic devices are an even more recent innovation than SiC devices. The advancement in GaN for power electronics in the last decade has been characterized not by iteration on existing products but by a move from microwave applications with low-voltage requirements to low-cost lateral power devices with higher breakdown voltages.<sup>29</sup> These devices are the result of significant public and private research activities focusing on the development of GaN transistors on Si substrates. This work culminated in the announcement of the first GaN-on-Si HEMT by International Rectifier in 2010. International Rectifier's announcement was quickly followed by a public device release from Efficient Power Conversion Corporation (EPC). These milestones and others are shown in Figure 6.N.4. One of the developments that made this shift possible was the improvement in GaN-on-Si epitaxy techniques.<sup>7</sup> The new technique allows for the creation of epitaxial material with slightly inferior properties, but at orders of magnitude lower cost than GaN on bulk SiC or GaN epitaxies: reported costs were \$100 for a four-inch Si wafer for GaN epitaxy, as opposed to \$3,130 for a four-inch high performance SiC wafer or \$7,500 for a GaN wafer for GaN epitaxy.<sup>29</sup>

**Figure 6.N.4** Milestones in GaN Power Electronics Development<sup>7</sup>

Credit: IHS Technology (<http://technology.ihs.com>): *The World Market for Silicon Carbide & Gallium Nitride Power Semiconductors - 2013 Edition*

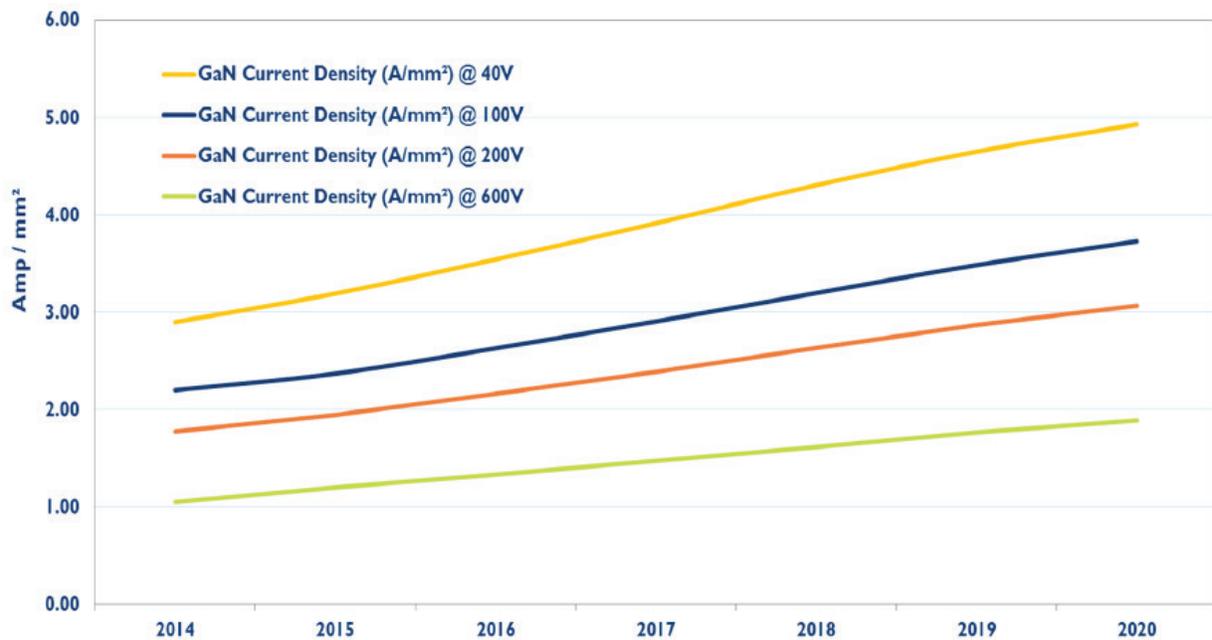




Average current density for GaN devices is expected to increase over the next several years, as shown in Figure 6.N.5.<sup>23</sup> Recent GaN advances include the move to larger GaN-on-Si epitaxial wafers. Eight-inch wafers have been demonstrated,<sup>30</sup> and gate injection transistors (GITs) have been developed to complement traditional HEMT devices in the GaN power electronics space. GITs are well suited to the higher end of the GaN transistor voltage range, with some of the first devices rated for 600 V.

**Figure 6.N.5** Forecast of GaN-on-Si HEMT Device Average Current Density Through 2020<sup>26</sup>

Credit: Yole Developpement



In the private sector, a majority of the companies that have brought GaN devices to market are based in the United States. Table 6.N.2 shows the development stage and product focus for companies involved with GaN research.<sup>31</sup> Of the five companies that had products on the market in 2013, four (all except MicroGaN) were based in the United States. Three of the companies—EPC, Transphorm, and MicroGaN—are primarily focused on GaN for power electronics. Many major companies have GaN devices in development, including traditional Si-focused device companies such as Texas Instruments and Panasonic.



**Table 6.N.2** Development Stages and Product Types for Companies Involved in GaN Power Electronics<sup>31\*</sup>

Vendor	Products		Technology	Development		Product Types		
	Open market	Closed market	Foundry services	Collaborative	In-house	Discrete	IC	Module
IRF		●				●		●
EPC	●					●		
Transphorm		●				●		●
Fujitsu					●			
Sanken					●	●	●	
MicroGaN		●	●			●		
Infineon					●	●		
HRL					●	●		
Panasonic					●	●	●	
STM					●	●	○	
RFMD		●	●			●		
Toshiba					●	●		
GaN Systems					●	●		
NXP				●		○		
TI					●	●	●	
Freescall					●	●	○	
Powdec					●	●		
Renesas					●	●		
Furukawa					●	●		
POWI					●		●	
ON Semi				●		●	○	
Intersil				●	●		●	
Alpha & Omega					●	●		

\*Note: Filled circles indicate actual production of products and product types (as of Dec. 2012); open circles indicate future potential.

## Technology Needs

The greatest challenge to the adoption of WBG components in power electronics is their high cost. Substrate materials account for 30%–50% of the cost of an SiC device, while traditional Si-based power device substrates account for only 5%–7%.<sup>7</sup> However, it is possible to achieve higher currents in a SiC wafer compared to the same size Si wafer because of the lower specific on-resistance of the SiC devices. Therefore, although SiC substrates and epitaxial layers are currently more expensive than Si, they can still compete with Si devices costs on a cost/area basis. Issues such as localized heating and parasitic current leaking in the vicinity of impurities and crystal defects (exacerbated at high currents) must be addressed to take full advantage of SiC's low specific on-resistance.<sup>32</sup>

In the case of SiC, cost reduction can be realized through high-volume processing of wafers. PowerAmerica is working to lower upfront costs of WBG power electronics by investing in a commercial foundry model.<sup>33</sup> This will allow small “fabless” companies to enter the market, develop improved device processing steps, and produce devices at lower costs. Such a model could play a foundational role in the rise of WBG semiconductors in the same way that the Metal Oxide Semiconductor Implementation System (MOSIS) did for Si integrated circuits (ICs).<sup>34</sup> These small fabless companies would then rely on pure-play semiconductor foundries that do not offer IC products of their own design, but instead operate semiconductor fabrication plants focused on producing ICs for other companies.<sup>35</sup>

Material quality still remains an area for improvement. SiC MOSFETs are by far the most prominent WBG switching devices used today, but they are limited by metal-oxide-semiconductor interface quality issues. Problems with the interface can lead to variability in threshold voltages as well as low device reliability. This has led to the limited adoption of SiC junction gate field-effect transistors and bipolar junction transistors compared to SiC MOSFETs, which otherwise would have been the preferable solution.

Because of the relatively low cost of power electronics-grade Si substrates, substrate cost is not a great concern for GaN-on-Si devices, but there are efforts underway to decrease costs further. One mechanism would be to reduce the thickness of eight-inch Si wafers to around 0.027 inches (675 microns). This would allow GaN-on-Si wafers to be processed on existing complementary metal-oxide semiconductor (CMOS) IC production lines, avoiding the need for new production equipment. GaN-on-Si epitaxial layers are currently more expensive and may be lower quality than GaN-on-SiC epitaxial layers, but it is expected that this will be addressed over time as manufacturing volumes increase.

The application of GaN on Si substrates necessitates a nucleation layer for GaN crystal growth—typically aluminum nitride (AlN).<sup>36</sup> Cost-effective growth of high-quality nucleation layers is difficult because a pre-reaction between the gases used to deposit the nucleation layer at high pressure leads to a trade-off between growth rate and quality.<sup>37</sup> Moreover, interfacial charges between AlN and adjacent materials can prevent normally off device operation.<sup>38</sup> The primary issue with the acceptance of GaN lateral devices is the fact that they are generally normally on, which means the devices are conducting when the gate is grounded. This is not acceptable to the power electronics industry at large. A cascode circuit configuration with a Si low-voltage MOSFET solves this problem but results in extra switching losses, making it difficult to operate at very high switching frequencies; this can negate the primary advantage of GaN devices. A true GaN enhancement mode transistor is needed, with a threshold voltage of 3–4V and >10V gate voltage operation. A limited offering of enhancement mode devices is available from select vendors. These devices generally have approximately 30% higher specific on resistance compared to normally on devices. This reduces their net efficiency advantage over normally on devices. The use of a gate dielectric necessary to achieve enhancement mode operation also leads to instabilities that are currently being addressed by researchers.

GaN power device research needs include techniques to address the high strain in the GaN layers due to GaN's crystal lattice mismatch with Si. GaN's low thermal conductivity in comparison with SiC also needs to



be addressed in order to better characterize the temperature limits of high performance GaN devices.<sup>7</sup> If the price of bulk GaN wafers were sufficiently reduced, vertical device architectures could be utilized as opposed to lateral devices with GaN-on-Si. Vertical devices would allow GaN to be used in higher power applications above 100 kW.<sup>39</sup> It is anticipated that over the next few years the rapidly increasing demand for LED lighting solutions will help drive down the cost of GaN wafers.

According to Shenai,<sup>40</sup> though horizontal GaN devices have excellent theoretical performance characteristics, power devices with this architecture are vulnerable to surface breakdown and are difficult to scale to high voltages or currents. Vertical GaN devices are therefore necessary for applications with voltages above roughly 600 V. Research and characterization efforts needed to enable new techniques for producing GaN substrates include studies of the relationship between dislocation density and leakage current with GaN substrates produced through the sodium flux method.<sup>41</sup> Progress in vertical GaN power devices could be aided by progress in vertical GaN LED research and development (R&D), as vertical architectures have been embraced for LEDs due to better current spreading and heat dissipation behavior.<sup>42</sup> Vertical GaN R&D has also produced significant accomplishments in high voltage power electronic devices, including vertical GaN transistors with blocking voltages of 1.6 kV and vertical Schottky diodes with blocking voltages of 2 kV.<sup>43</sup>

Advancements toward vertical GaN devices have been recognized as important by the Department of Energy (DOE) Advanced Research Projects Agency-Energy (ARPA-E). Research into the materials and manufacturing processes necessary for these devices figures prominently in the agency's "Strategies for Wide Bandgap, Inexpensive Transistors for Controlling High-Efficiency Systems" (SWITCHES) program. SWITCHES is a \$27 million program announced in October 2013 and is funding 14 projects, involving universities, national laboratories, and private companies.<sup>44</sup> Projects focused on vertical GaN technology within this program include partnerships led by the University of California, Santa Barbara, with Arizona State University, Transphorm, and the U.S. Naval Research Laboratory;<sup>45</sup> Avogy in collaboration with ABB, North Carolina State University, Oak Ridge National Laboratory (ORNL), and Soraa; and several others involving Columbia University, HRL Laboratories, and SixPoint Materials.<sup>45</sup> Transphorm and Fujitsu Semiconductor have recently announced the start of mass production of Transphorm's GaN power devices for switching applications.<sup>46</sup> The start of mass production in a CMOS IC production line is a significant step forward toward achieving the widespread use of GaN power devices.

The rest of the circuit is also critical to the large-scale implementation of more efficient WBG power systems as a result of their ability to operate at high frequency, specifically the magnetic systems. While WBG semiconductors can easily switch high voltages and currents at MHz frequencies, the transformers and inductors have much higher losses at those frequencies. New circuit designs, better soft magnet alloys, and alternate transformer designs are all potential areas for development.

Long-term reliability data is also needed to gain marketplace acceptance for both SiC and GaN power electronics devices. Fundamental reliability research at the device level needs to be performed as well as new packaging methods developed that will allow WBG devices to operate at their full potential. The only commercial WBG power devices with more than 10 years of market performance are SiC Schottky diodes. As such, they are the only devices with proof of their reliability on the scale required for high-end applications. Standardization of tests to ensure greater reliability for new transistor designs will be useful, but large-scale adoption will not occur until lifetimes in excess of 10 years can be conclusively proven in demanding applications.

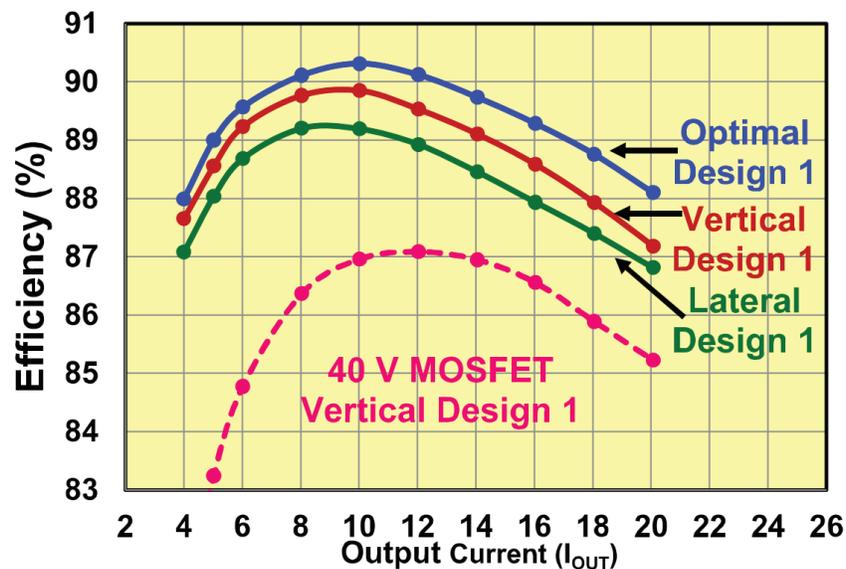
### System Integration Needs

For both SiC and GaN power electronics devices, their benefits will not be fully realized if they are treated as drop-in replacements for Si devices. Instead, packaging techniques and circuit designs are needed that optimize their properties, resulting in minimization of size and costs of cooling and auxiliary circuit components.

At the power module level, there is a need for new materials and packaging methods to enable and capture the benefits of the higher temperature capabilities of WBG power devices. Higher power and temperature operation will necessitate robust bonding mechanisms for both the dies and module substrates that can withstand repeated power and temperature cycling. Higher switching frequencies introduce new concerns at both the power module and board levels. Parasitic inductances and resistances can result in significant power losses when the circuit is switched at high frequencies.<sup>47</sup> Attention to the minimization of parasitic properties can also significantly decrease voltage and current overshoots, reducing electromagnetic interference (EMI) filter requirements, circuit volume, and cost. Figure 6.N.6 shows the effect of parasitic inductance for a 12 V to 1.2 V buck converter circuit with two EPC enhancement-mode GaN field-effect transistors (EGaN FETs) operated at 1 MHz by comparing the efficiency of an optimized layout to more traditional vertical or lateral circuit designs.

**Figure 6.N.6** Efficiency Impact of Board Design to Avoid Parasitic Inductance<sup>47</sup>

Credit: Efficient Power Conversion Corporation



At the system level, the use of WBG devices can significantly reduce cooling requirements. Reducing the size of heat sinks, radiators, pumps, and piping can result in cost savings from both a materials and manufacturing perspective as well as ancillary power savings, translating to higher system-level efficiency.

The key motivation for leveraging the frequency increase facilitated by WBG semiconductors use is miniaturization, integration, and improved performance (bandwidth). Passive energy storage components (especially magnetics) are the dominant constraints. Magnetic core materials largely impact frequency scaling; however, integration/batch fabrication of passive components imposes further challenges.

Though high-frequency operation can improve system performance in a number of ways (reduced equipment size, reduced losses in magnetic components, etc.), it is challenging to achieve high power conversion efficiencies at frequencies higher than 500 kHz in conventional discrete systems because of large losses in the long interconnects and parasitic inductance from their long bond wires. It is also challenging in conventional discrete systems to use high external current gate drivers due to high gate drive losses. Although this suggests that progressively moving into integrated inductors could be advantageous, there are currently several challenges in inductor integration, such as the fact that integrated air core inductors typically do not have sufficient inductance to meet circuit requirements in the limited volume available from the device geometry.



The production of integrated inductors with magnetic cores may require high-temperature processing such as the co-fired ceramic process, but the high temperatures in these processes could damage the fabricated device with the integrated power electronics.

### Potential for Improvements

There is opportunity for reduction in the price of WBG power electronic devices. An IHS market report from 2013 forecasted average prices for different WBG devices.<sup>7</sup> SiC Schottky diodes rated at 20A, used for industrial motor drives, are expected to decline in price from about \$0.22/A in 2014 to \$0.10/A in 2022. SiC MOSFET prices for similar applications are expected to decline in price from about \$0.96/A to \$0.55/A, and GaN transistors are expected to decline in price from about \$0.35/A to \$0.12/A. The fabless foundry model that PowerAmerica is pursuing could play a key role in achieving these cost reductions.

Currently, a dedicated foundry requires a \$100–\$200 million investment and cannot become profitable unless fully loaded with, for example, at least 10,000 wafer starts a month. Because the present demand for WBG devices is low (approximately 100–200 wafer starts per month), the investment in a dedicated foundry cannot be justified.

The idea of using an established Si commercial foundry for the manufacture of WBG devices was proposed by DOE<sup>48</sup> and is the concept currently being implemented by PowerAmerica. Because approximately 90% of the processes involved with the manufacture of WBG devices are similar to Si processes, processing costs can be significantly reduced by using idle time in the Si foundry for WBG fabrication runs. This also takes advantage of existing equipment and reduced overhead costs of a commercial foundry. The remaining 10% of processes that are unique to WBG manufacturing can be implemented at a cost of roughly \$10 million.<sup>49</sup> The commercial foundry approach will then have the potential to provide a fabless model to many companies, universities, and laboratories. MICREL and X-Tab are the two major domestic foundries working on six-inch Si wafers today. The commercial foundry approach will facilitate innovations in designs and processes, thereby attracting new venture capital. As little as \$1–\$2 million investment will be required by new companies as opposed to having to invest \$100–\$200 million in a dedicated foundry. As more clients take advantage of the opportunities afforded by their involvement in a commercial foundry, device cost per amp will be significantly reduced due to increased aggregated volume production. Current dedicated foundries produce SiC devices (1200 V, 20 A SiC MOSFET) at roughly \$0.54/A or five times the cost of Si devices (\$0.10/A). Assuming substrate and epitaxial layer costs will decline at higher volumes, costs could drop to as little as \$0.074/A.<sup>50</sup> It is projected that through technological innovations and the move to eight-inch wafers,<sup>51</sup> the cost of an SiC MOSFET could become competitive with the current cost of Si devices within five years.<sup>50</sup> Even with six-inch wafers, modest technology advancements, the commercial foundry approach, and an increase in volume can make SiC competitive with Si.

GaN could be a drop-in replacement for existing Si FETs, but the incremental benefit of that approach does not take advantage of the full value that GaN can offer. Instead, GaN can be used in advanced topologies, enabling higher performance, efficiency, and power density than current Si FETs. A promising area for GaN research is based on the fact that present-day GaN power devices are being developed on Si substrates. This creates the opportunity to develop high frequency GaN power transistors alongside driver ICs on a common Si substrate. In addition to lowering costs, this would address one of the major hurdles for integrating GaN into power electronic devices, which is that GaN transistors are perceived as being difficult to drive.<sup>7</sup> However, Si ICs for these duplexed devices would still be limited in the switching speeds they could achieve. Better GaN processing techniques would allow for the creation of GaN ICs that could reach higher switching speeds. These improved techniques could be applied to more cost-effective growth of bulk GaN wafers in order to produce vertical GaN-on-GaN devices that could be used in higher power (100 kW or greater) applications.<sup>52</sup>

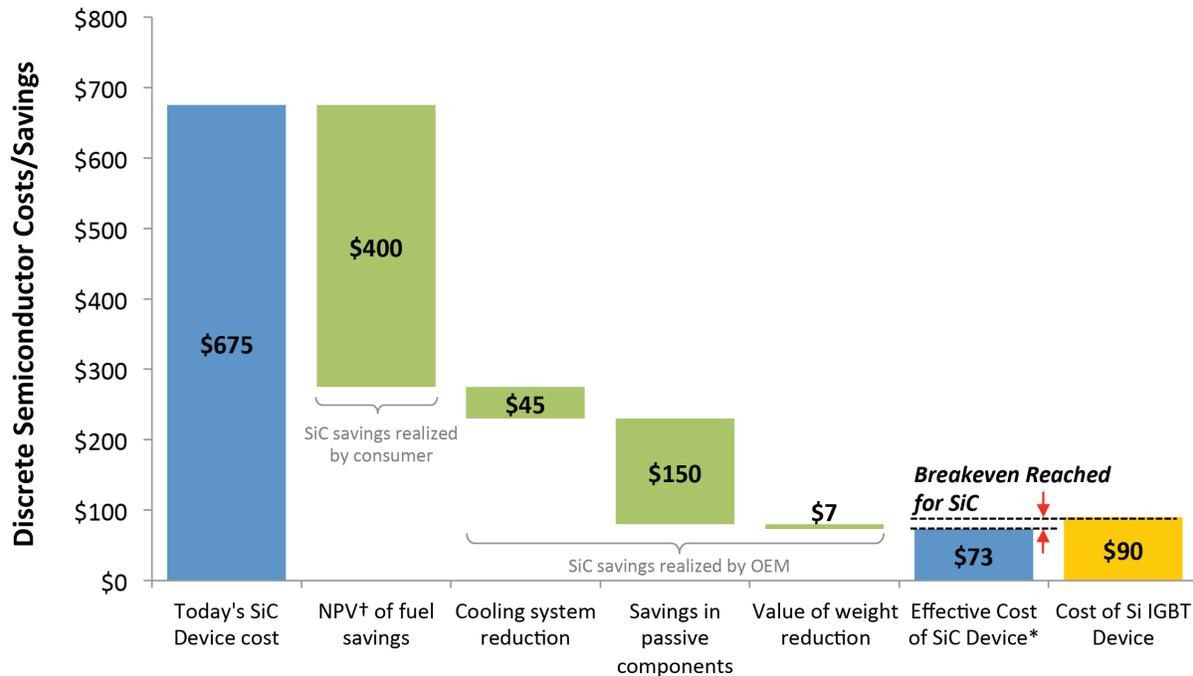
## Potential Impacts

The value proposition for WBG devices consists of the following four major points:

- *Reduced energy and material costs.* Because WBG semiconductors are inherently more efficient than Si devices, less energy is expended as heat, resulting in smaller system sizes and lower material costs.
- *Higher power density (smaller volume).* Higher switching frequencies and operational temperatures than Si result in lower cooling requirements, smaller heat sinks, and reduced magnetics.
- *Higher switching frequency.* The higher switching frequencies for WBG devices allow smaller inductors and capacitors to be used in power circuits. Inductance and capacitance scale with the frequency—a 10x increase in frequency produces a 10x decrease in the required capacitance and inductance. This can result in an enormous decrease in weight and volume as well as cost. In addition, higher frequency can result in less acoustic noise in motor drive applications.<sup>7</sup>
- *Lower system cost.* While WBG semiconductors are generally higher cost than Si, system level cost reductions are sometimes possible through the use of WBG by reducing the size/costs of other components, such as inductive and capacitive circuit elements, filters, and cooling.

Hull<sup>4</sup> illustrates the first three of these points in a discussion of a 30 hp electric inverter motor where Si power electronic devices in the motor's VFD are replaced with SiC devices. The 3-phase drive uses a 1,200 V, 50 A, six transistor module. The higher cost of the WBG-based VFD in this example is recovered in 6 to 24 months, depending on the assumed per-kilowatt-hour cost of electricity. Such a payback period is acceptable when VFDs that are designed to have a life in excess of 10 years are considered. Regarding power density, Si-based VFDs for large motors occupy significant plant floor space, which could be substantially reduced with the use of WBG devices. In the motor example, the SiC solution allows for the heat sink to be reduced to one-third of its former size. A leading manufacturer of VFDs confirms that higher power density VFDs are an important value proposition for its customers.<sup>53</sup> Regarding switching frequency, the SiC solution allows for a motor output of 30 hp motor output power over a broad range of switching frequencies. The SiC switches could drive the motor at 30 hp for any switching frequency in the range of 8 kilohertz (kHz) up to 16 kHz. Losses prevented the Si-based VFD from driving the motor at 30 hp with frequencies of 12.5 kHz or higher. At 16 kHz switching, the motor output is limited to only 20 hp with the Si solution. Hull<sup>4</sup> provides an example of the fourth point, system cost reduction, with a case where the cost of a SiC-based boost converter is reduced by 20% over its Si counterpart through reduced inductor and heat sink sizes and costs.

SiC devices in hybrid vehicle motor inverters have the potential for additional impact because of their improved high-temperature properties over Si devices. In most hybrid vehicles, the inverter is near the internal combustion engine and requires a separate cooling system. If SiC devices are used in the inverter, this could allow for the inverter to be kept at a temperature nearer to the engine temperature, which would allow for the use of a single cooling system.<sup>54</sup> According to McKinsey & Company,<sup>55</sup> when all the cost savings for the original equipment manufacturer (OEM) and consumer are considered, the value proposition for SiC devices in hybrid electric vehicle (HEV) inverters is already better than the value proposition for Si insulated-gate bipolar transistors (IGBTs), despite the higher device cost for SiC, as shown in Figure 6.N.7. Fuel savings (assumed over an eight-year vehicle life at \$3/gallon gasoline) are realized by consumers, while savings from weight reduction, reduced passive component requirements, and reduced cooling system requirements are realized by OEMs.

**Figure 6.N.7** Value Advantage Gained from Use of SiC Transistors Compared to IGBTs for Hybrid-Electric Vehicle (HEV) Inverters, Based on the Entire Value Chain<sup>55</sup>

\* when considering value chain savings

† NPV = Net Present Value

Information to quantify the benefits of switching from Si-based superjunction (SJ) MOSFETs to GaN-based HEMTs on a system level is not widely available. One available comparison is for a 250 W internal power supply of an all-in-one iMac desktop computer.<sup>56</sup> The GaN-based power supply used three Transphorm HEMTs and switched at 200 kHz as opposed to 50–80 kHz for the Si-based iMac supply. This allowed for a 55% size reduction as well as efficiency increases from 82% to 85% for a 15 W output supply and from 92% to 94% for a supply with 180 W output.

More empirical results of efficiency benefits of GaN HEMTs exist for individual power supply circuits, such as power factor correction (PFC) and DC–DC conversion stages. Zhang et al. have discussed the efficiency gains for a GaN based Buck-PFC circuit as might be used in a 90 W laptop adapter.<sup>57</sup> They compared the 115 Volt AC performance of a Buck-PFC evaluation module from Texas Instruments with a GaN HEMT and SiC Schottky diode against the same module with an SJ-MOSFET and a Si Hyperfast diode. The WBG module allowed for a 1%–2% efficiency improvement, with more pronounced gains at lower power levels. A number of sources claim transistor efficiency improvements of 3%–7%<sup>47, 58</sup> and DC-DC conversion efficiency improvements of 2%–4%,<sup>5, 6</sup> with the greatest improvements at the low end of a device's power range.

## Enabling R&D

Based on an ORNL survey of WBG industry contacts, there appears to be a common perception that there are not enough engineers or physicists with adequate training to address WBG material production issues on the scale necessary for greater commercialization.<sup>59</sup> There was also a perception that materials research in this area is not encouraged and that an innovation center specifically focused on next-generation materials would help in the development of WBG power electronics technology in the United States. However, it should be noted that the Department of Defense has heavily funded basic materials research in GaN and SiC over the last 20 years.<sup>16, 60</sup> During this period, wafer diameters have increased from one inch to six inches, along with greatly improved material quality. Therefore, it was deemed important to focus on reducing the cost of device fabrication, packaging, and power electronics rather than the further development of materials. The material quality is considered sufficient for 150 mm substrates and epitaxial layers to manufacture 600 V to 15 kV devices. A widespread adoption of WBG devices is expected to occur as a result of reduced chip cost; this will create a market pull for increasing the substrate diameter to 200 mm (7.9 inches) and even 250 mm (9.8 inches), which is expected to happen without external funding.

The United States has substantial presence in developing WBG devices, but most WBG device end users are outside. Currently, the major pure-play semiconductor foundries are located either in China or Taiwan, in close proximity to most of the manufacturers using these devices. This presents a huge challenge for U.S.-based manufacturers, as these foundries will facilitate the early manufacturing of WBG semiconductors. The ORNL survey found a general perception that the U.S. was not keeping up in WBG research, manufacturing, and use, underscoring the risk of the U.S. being eclipsed in the critical area of power electronics by China and other countries. These and other concerns motivated the creation of PowerAmerica, which has a goal of providing an ecosystem for power electronics manufacturing and R&D in the United States. The survey of U.S. WBG suppliers identified the following key needs to improve the environment for WBG innovation:<sup>59</sup>

- sufficient funding of research;
- tax policies that would encourage internal research and the purchasing of capital equipment for WBG processing; and
- mechanisms to stem the flow of intellectual property overseas that was directly or indirectly developed through U.S. government funding.

Regulations and standards are an important means to encourage efficiency improvements in power electronics for consumer goods. It can be difficult for a manufacturer to make the case for choosing a more expensive part, even when it would lead to greater product efficiency. One participant in the aforementioned survey stated that the use of SiC components in a refrigerator compressor drive could cut energy losses by 25%, but that manufacturers had no incentive to pay the few extra dollars to add them because manufacturers do not pay the operating costs.

In addition to PowerAmerica, other public/private partnerships are being formed to advance WBG power electronics manufacturing in the United States. As mentioned earlier, New York State is partnering with a large number of private companies, led by GE and including Lockheed Martin, to launch the NY-PEMC. The public-private partnership will invest more than \$500 million over five years, focused on the development of next-generation WBG semiconductor materials and processes at the state-owned R&D facility in Albany, New York. GE will be a lead partner in the fabrication plant, housed at the newly merged State University of New York Colleges of Nanoscale Science and Engineering NanoTech Complex, which aims to develop and produce low-cost six-inch SiC wafers.<sup>61</sup>

NextEnergy and the Power Electronics Industry Collaborative are focusing activities on identifying domestic challenges, opportunities, and pathways forward in WBG technologies for the power electronics industry.<sup>62</sup> In an industry-led workshop held in 2013, they recommended actions to address specific gaps, including adopting



an application-driven approach, addressing the lack of adequate testing procedures to demonstrate reliability, and other methods for accelerating innovation and reducing innovation risk. Other recommendations included the strengthening of power electronics expertise and means of reducing the talent deficit in the United States.

### **Risk and Uncertainty, and Other Considerations**

The previous section on “Enabling R&D” highlighted issues and associated approaches that can help mitigate risks regarding public, private, and public/private partnership investments in manufacturing WBG technologies in the U.S. More fundamentally, there are also risks that could impede the uptake of WBG technologies, including the possibility that device costs might never be low enough, or reliability high enough, for widespread penetration into targeted applications. To help address cost, PowerAmerica was established with the stated goal of making WBG power electronics cost-competitive within five years. For reliability, recent SiC device reliability data from industrial leaders, including GE and Cree, Inc., has shown marked improvement in this area and is helping to alleviate these concerns.

There are also external factors that could affect the penetration of WBG devices in power electronics applications. One is that emerging markets could impose less rigorous efficiency standards, which would limit the motivation for manufacturers to use high-efficiency power electronics. Another is that electricity prices could fall low enough that businesses that might otherwise have chosen high-efficiency power electronics will be dissuaded from doing so due to long payback periods.

## Case Studies

### AC Adapter Global Energy Consumption

The Electric Power Research Institute (EPRI) estimated that 130 TWh of electricity was consumed by U.S. residential electronics in 2008.<sup>63</sup> A 2009 report for the International Energy Agency assessed the global energy consumption of external power supplies (EPSs) for electronic devices such as laptops and mobile phones in 2008 at nearly 50 TWh, or about 1% of global electricity consumption.<sup>64</sup> Mobile phones, MP3s, and AC adapters were estimated to use 45% of this energy, or roughly 23 TWh, which also accounted for the losses between the AC power source and the electronic device.

Many assumptions must be made in order to estimate the energy use of AC adapters or EPSs. A report for DOE classified four broad EPS modes—active, no-load, off, and unplugged—depending on the state of the EPS connection to the mains, its connection to the application, and the state of the EPS on/off switch.<sup>65</sup> The active mode classification can be broken down further. Six states were discussed within the active mode for laptop computers, ranging from using 66% of the EPS nameplate output power when the computer is on and the battery is being charged to 0.6% nameplate power when the computer is off with a fully charged battery. These usage profiles result in an average power much lower than the rated power of the adapter. In fact, the capacity factors (the ratios of the average annual power output to the rated power output) for the EPSs discussed were around 13%. The report also discussed annual sales for active mode EPSs with various efficiency levels from 85% to 92%. For the annual U.S. shipped stock of 36.7 million laptops and netbooks in this study, the average active mode efficiency was 87% and the annual power consumption of all units was 404 million kWh.

Based on the earlier discussion under *Potential Impacts*, it is reasonable to assume that the introduction of GaN HEMTs to laptop adapters could increase laptop efficiency by 3%. This is a conservative assumption because laptop adapters typically provide a small percentage of their rated power, and the benefits of GaN HEMTs are more pronounced at low power levels. The effects of a 3% efficiency increase on the global stock of laptop adapters can be seen in Table 6.N.3. The 2014 sales numbers in this table were based on Eykyn<sup>66</sup> and Gartner.<sup>67</sup> The three-year projected adapter life used to determine the global stock was based on Boyd.<sup>68</sup> The 1,904 million kWh saved for laptop adapters would amount to \$114 million, assuming a cost of \$0.06/kWh.

Table 6.N.3 also shows the same calculation for tablet and cell phone adapters. These adapters have significant standby power losses in addition to their active mode losses.<sup>65</sup> The implementation of GaN HEMTs in these adapters can reduce annual per-unit losses by 23%, corresponding to a laptop adapter efficiency increase from 87% to 90%. The total annual savings of 7,670 million kWh from the use of WBG transistors in these adapters is on the scale of the annual output of a mid-sized coal power plant.

In addition to power savings, an important benefit of using high frequency GaN electronics is that the adapter size can be reduced by 10x. Many consumers will be willing to pay the incremental higher cost for a much smaller adapter, helping to drive up volume sales, achieving corresponding cost reductions. When the cost of GaN devices eventually reaches the cost-sensitive price point that enables their introduction into flat-screen TV power supplies, substantial energy savings will be achieved through their higher efficiencies.

**Table 6.N.3** Potential Impact of GaN WBG Components on Global Energy Use<sup>65, 66, 67, 68</sup>

Transistor Material	Application	Average power rating (W) <sup>65</sup>	Average active mode efficiency <sup>65</sup>	Annual loss per unit (kWh)	2014 global sales (millions) <sup>66, 67</sup>	Assumed product life (yrs) <sup>68</sup>	Global stock (million units in service)	Annual electricity loss by global stock (million kWh)
Si	Laptop	60	87%	11.0	\$250	3	750	8,250
	Tablet	12	80%	1.9	\$250	3	750	1,425
	Cell phone	5	63%	4.2	\$1,870	3	5,610	23,562
	<b>Total</b>							
GaN	Laptop	60	90%	8.5	\$250	3	750	6,346
	Tablet	12	85%	1.5	\$250	3	750	1,096
	Cell phone	5	72%	3.2	\$1,870	3	5,610	18,125
	<b>Total</b>							
<b>Savings Enabled by GaN WBG Semiconductor (million kWh/year)</b>								<b>7,670</b>
<b>Savings Enabled by GaN WBG Semiconductor (TBtu/year, end use energy)</b>								<b>26.2</b>

## Data Centers

Data centers in the United States consumed approximately 2.2% of the total U.S. electricity in 2012, amounting to 84 billion kWh (288 TBtu).<sup>69, 70</sup> Power conversion activities inside an average data center (power use effectiveness = 1.8) account for 10.4% of the energy consumed in the average data center.<sup>71</sup> Switching from Si-based devices to WBG-based devices could increase conversion efficiency from 90% to 98%.<sup>72</sup> This would amount to an 8.3% reduction in energy usage by data center power electronics.

Beyond this direct reduction in energy usage, the losses avoided would have increased the cooling load of the data center itself. Assuming that cooling itself generates no heat, the heating load of the data center is reduced by 12.7%. This represents an overall 4.4% energy savings of a data center.

Adding these two energy saving opportunities means that 12.7% (8.3% + 4.4%) of energy could be saved. This equates to 10.7 billion kWh (36.6 TBtu of end use energy or roughly 111 TBtu of primary energy) of energy savings from the full implementation of WBG devices in data centers.

## Increased Penetration of Variable Frequency Drives

As stated in the introduction, motor drives are expected to be an important application for SiC power electronic devices. If SiC power electronics can improve the system level cost and/or power density of VFDs sufficiently to significantly increase the adoption of VFDs for industrial motor drives, this could have profound effects on the world's electrical energy use. When motors with a variable load do not have some sort of adjustable speed drive (ASD)—typically a VFD for industrial AC motors—to match the motor output to the load, the output of the motor must be throttled. This is an inherent inefficiency that could be addressed with greater adoption of VFDs. An example of an AC motor-driven system without a VFD is a fan system where airflow is controlled by dampers. In such a system, more airflow than is actually needed is generated by the fan and dampers are used to control excess airflow after energy has already been expended by the motor. In addition, this process usually moves the fan off its design point, thereby further reducing overall efficiency. By contrast, incorporation of a VFD in such a system allows for precise control of motor speed such that it is exactly matched with airflow requirements, thereby saving energy. An important takeaway from this example is that most energy savings come from the design and control decisions made possible by a switch to VFDs, not from the VFDs themselves.

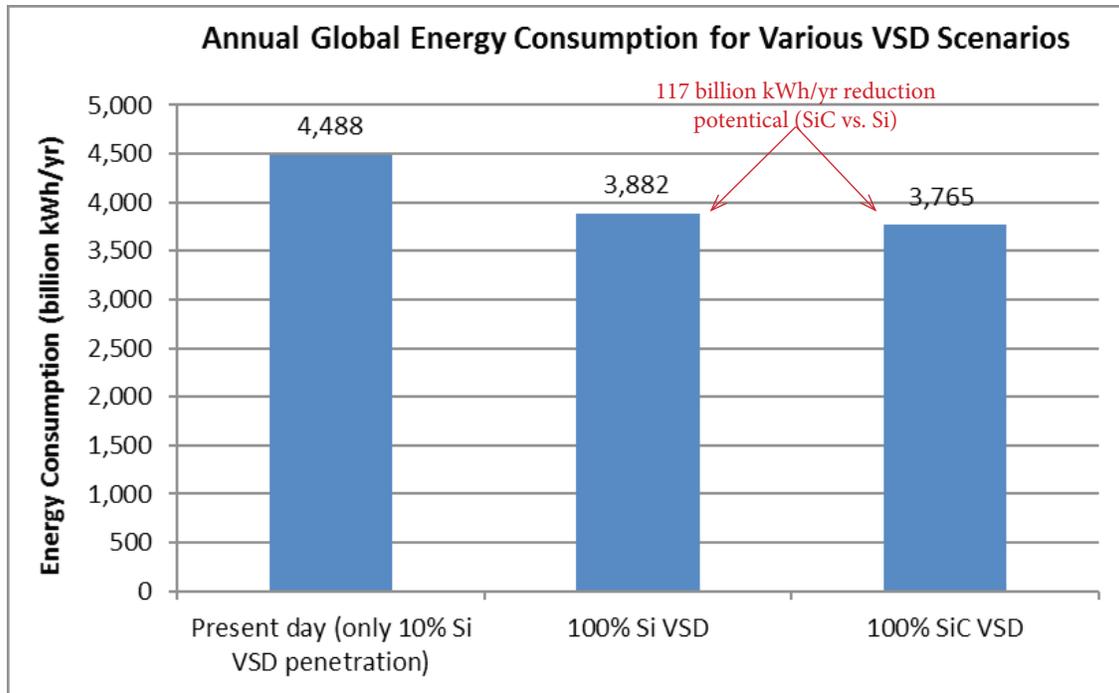
Estimating the potential energy savings from increased adoption of VFDs in the global stock of electric motors is difficult because the potential benefits vary from one application to the next and the present penetration rate of VFDs is uncertain. For the global stock of refrigeration, air compressor, and pump/fan applications, average energy savings of 10%, 15%, and 20% have been estimated through the use of VFDs, respectively.<sup>73</sup> The current penetration rate of VFDs for electric motors is thought to be quite small at less than 10% in industrial applications.<sup>74</sup> If it is assumed that VFDs can achieve an average energy savings of 15% in the global stock of industrial electric motors and that 90% of such motors are not presently equipped with VFDs, then electricity consumption by motor-driven equipment in the industrial sector could potentially be reduced from approximately 4,490 billion kWh/yr to 3,880 billion kWh/year if 100% penetration were achieved. Note that this is an optimistic projection because motors that run at a consistent speed and do not use throttling valves, vanes, or other such controls, are not likely to benefit from VFDs.

Currently, VFDs use conventional Si-based semiconductors, which are less efficient than WBG semiconductors. As an extension of the preceding energy estimate, if it is assumed that Si VFDs have an average efficiency of 94.5% with 100% penetration of Si VFDs, global electricity losses in industrial electric motor drives can be estimated to be 214 TWh/year ( $3,882 \text{ TWh/year} \times 5.5\%$ ). If WBG semiconductors were to replace their Si counterparts, they could reduce VSD losses by 55%; 100% penetration of WBG-based VSDs would lead to 96 TWh/year ( $214 \text{ TWh/year} \times 45\%$ ) of VFD losses. In other words, WBG semiconductors could offer additional electricity savings of 117 TWh/year (399 Tbtu) over traditional Si-based VFDs, assuming 100% penetration of WBG VFDs as shown in Figure 6.N.8.

In summary, if SiC VFDs achieved 100% adoption for relevant motors systems, the global energy savings could be 723 billion kWh/year. Again, caution is required with this estimate because there are motor systems that would not benefit from VFD installation.



**Figure 6.N.8** Global Electricity Reduction Potential in Industrial Electric Motors from Si- and WBG-based VFDs



## Renewable Energy Generation

Photovoltaic (PV) panels use the sun's radiation to directly generate DC current. This DC current is then inverted to AC to generate power suitable for the grid. In 2014, 15 billion kWh of power was generated by solar PV in the United States.<sup>76</sup> The energy savings rate of an SiC inverter over an Si inverter was found to be 3% (from 96% to 99% efficiency).<sup>77, 78, 79</sup> Rapid growth of PV installations thus offers significant energy gains and a market for SiC devices.

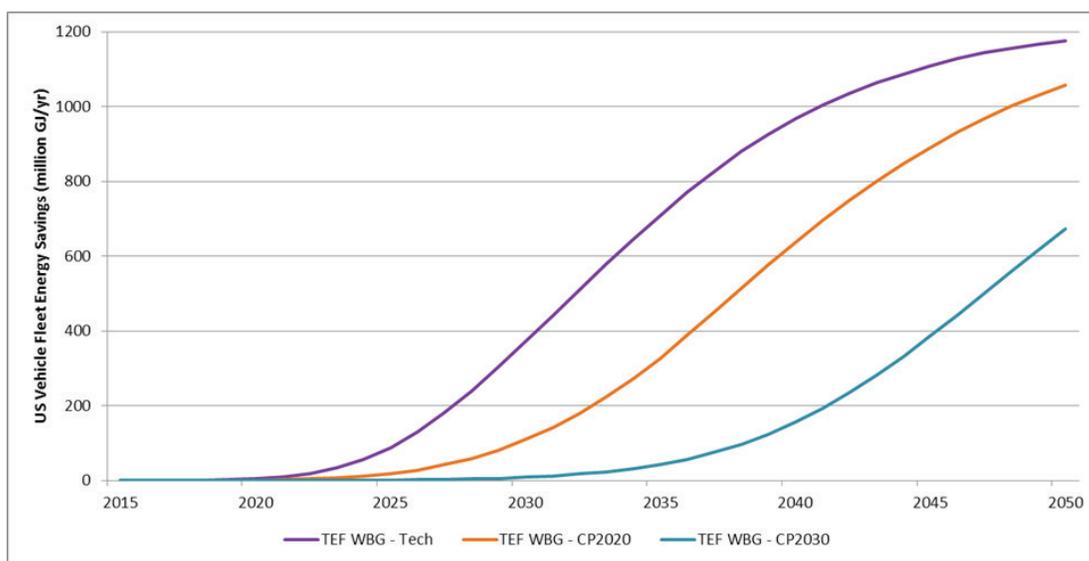
Modern wind turbines generate variable frequency AC power, depending on the wind speed. This current must be rectified to DC before being inverted back to AC at the necessary grid frequency. While this setup has losses associated with the conversion process, it permits wind turbines to operate at peak generating efficiency. Therefore, the deployment of WBG-based semiconductors would increase efficiency. In 2014, 182 billion kWh of energy was produced by wind turbines in the United States.<sup>76</sup> It is estimated that there would be a 4.6% absolute improvement in efficiency at 3 kHz switching speed, from 93.2% efficiency of Si to 97.8% efficiency of WBG system.<sup>80</sup> The additional annual generation possible from 100% implementation of SiC in wind turbine converters is 9 billion kWh. This potential would increase if the amount of electricity provided by renewable resources increases.

## Electric Vehicles

In electric vehicles, switching losses in power electronics degrade overall efficiency and range as useful energy is wasted as heat. With WBG semiconductors, switching losses are reduced compared with conventional Si switches on Si substrates so that overall fuel economy is improved. A recent study has explored the vehicle-level benefits of an all-SiC motor inverter in a 2004 Toyota Prius by simulation with Argonne National Laboratory's Powertrain System Analysis Toolkit, accounting for not only inverter level benefits but also the effects of higher efficiency, lighter weight, smaller volume, and other factors on overall fuel economy, which is improved from 3.94 L/100 km for the conventional Si/Si vehicle to 3.36 L/100 km for the SiC/SiC vehicle, a 14.7% improvement.<sup>81</sup> A life-cycle energy savings of SiC/SiC versus the conventional Si/Si power electronics in electric vehicles (i.e., HEV, plug-in hybrid electric vehicles, and battery-electric vehicles) has been considered, assuming an aggressive penetration of alternative vehicles as considered in the DOE Transportation Energy Futures (TEF) study.<sup>82</sup> In addition, the dependency of adoption of WBG semiconductors on their relative cost and performance benefits compared to their Si counterparts has been considered by assuming when the cost parity with Si will be achieved.

Figure 6.N.9 illustrates the potential impact on fleet-wide energy savings from WBG associated with different timelines for achieving cost parity. For the cost parity (CP) in 2020 scenario (TEF WBG—CP2020), an estimated 10% reduction in annual energy savings relative to the baseline technical potential scenario (TEF WBG—Tech) is predicted to be achieved by the end of the period (2050). The 2050 annual energy savings are predicted to be 43% less than technical potential if cost parity is delayed until 2030 (i.e., TEF WBG—CP2030). By 2050, domestic fleet energy savings are estimated to be in the range of 640–1,120 TBtu/year. Although SiC/SiC is more than 2.5x as energy intensive in the materials and manufacturing on a per cm<sup>2</sup> basis, vehicle use energy savings exceed the marginal increase in WBG semiconductor manufacturing energy consumption versus Si/Si by two orders of magnitude.

**Figure 6.N.9** U.S. Vehicle Fleet Energy Savings Potential in Electric Vehicles from SiC-based WBG Power Electronics



## Endnotes

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## Acronyms

<b>AC</b>	Alternating current
<b>ARPA-E</b>	Advanced Research Projects Agency-Energy
<b>ASD</b>	Adjustable speed drive
<b>CMOS</b>	Complementary metal-oxide semiconductor
<b>CP</b>	Cost parity
<b>DC</b>	Direct current
<b>DOE</b>	(U.S.) Department of Energy
<b>EMI</b>	Electromagnetic interference
<b>EPC</b>	Efficient Power Conversion Corporation
<b>EPS</b>	External power supply
<b>EPRI</b>	Electric Power Research Institute
<b>FET</b>	Field-effect transistor
<b>HEMT</b>	High-electron-mobility transistor
<b>HP</b>	Horsepower
<b>IC</b>	Integrated circuit
<b>LED</b>	Light Emitting Diode
<b>MOSIS</b>	Metal Oxide Semiconductor Implementation System
<b>MOSFET</b>	Metal-oxide-semiconductor field-effect transistor
<b>MRI</b>	Magnetic resonance imaging
<b>NY-PEMC</b>	New York Power Electronics Manufacturing Consortium
<b>OEM</b>	Original equipment manufacturer
<b>ORNL</b>	Oak Ridge National Laboratory
<b>PFC</b>	Power factor correction
<b>PV</b>	Photovoltaic
<b>RF</b>	Radio frequency
<b>SWITCHES</b>	Strategies for Wide Bandgap, Inexpensive Transistors for Controlling High-Efficiency Systems (ARPA-E program)



<b>TEF</b>	Transportation Energy Futures (U.S. DOE project)
<b>VFD</b>	Variable frequency drive
<b>WBG</b>	Wide bandgap

## Glossary

<b>Adjustable speed drive (ASD)</b>	A motor drive system that allows for the adjustment of output speed or frequency to meet the load demands of a machine such as a pump or fan. Variable frequency drives (VFDs) are a subset of ASDs for AC motors that change output speed via changes in AC frequency. Other types of ASDs can change output mechanically or hydraulically.
<b>Bandgap</b>	The span of energy between the valence band and the conduction band in a solid material. Electrons must gain this amount of energy to enter the conduction band from the valence band and allow current to flow. Conductors have negligible bandgaps, insulators have very wide bandgaps, and semiconductors have small but non-negligible bandgaps.
<b>Boost converter</b>	A circuit element that produces a higher output DC voltage than its DC input voltage.
<b>Buck converter</b>	A circuit element that produces a lower output DC voltage than its DC input voltage.
<b>Fabless foundry model</b>	A market model in which companies that own semiconductor device foundries contract to produce devices for fabless companies that design devices and market or integrate them, but don't have production capabilities.
<b>Gallium nitride (GaN)</b>	A wide bandgap (WBG) semiconductor material at a less advanced development stage than Silicon Carbide (SiC). GaN is used in high-electron-mobility transistors (HEMTs).
<b>High-electron-mobility transistor (HEMT)</b>	A high frequency transistor using a combination of semiconductors with different bandgaps. HEMTs are a target application for gallium nitride (GaN).
<b>Light emitting diode (LED)</b>	A semiconductor junction that allows current flow in one direction and produces monochromatic light in response to that current flow. LEDs produce light very efficiently compared to incandescent and (most) fluorescent light bulbs.
<b>Metal-oxide-semiconductor field-effect transistor (MOSFET)</b>	An electronic switch or amplifier driven by electric fields induced by a control voltage. MOSFETs require less control current and can deliver more load current than regular transistors.
<b>Micropipe</b>	A common defect in silicon carbide (SiC) substrate production resulting from the propagation of a screw dislocation.



<b>PowerAmerica</b>	A National Network for Manufacturing Innovation institute established in 2014 as a public-private partnership between the U.S. DOE, industry and academia. PowerAmerica is led by North Carolina State University, and is pursuing the key goal of making WBG power electronics cost-competitive within five years.
<b>Power factor correction (PFC)</b>	The action of bringing the power factor (the ratio of the real power seen by the load to the apparent power in a circuit) closer to one by supplying reactive power. This is a common function of elements of external power supplies (EPSs).
<b>Schottky diode</b>	A semiconductor diode with a low reverse bias voltage and fast switching capabilities. Schottky diodes represent an early application for Silicon Carbide (SiC).
<b>Silicon carbide (SiC)</b>	A wide bandgap (WBG) semiconductor material at a more advanced development stage than Gallium Nitride (GaN). SiC was first used in Schottky diodes and later used in MOSFETs.
<b>Variable frequency drive (VFD)</b>	An electric motor drive that alters the output speed of an AC motor by altering the frequency of the AC power input. VFDs are a subset of adjustable speed drives (ASDs). VFDs benefit from wide bandgap switches that can switch at higher frequency with fewer losses.
<b>Wide bandgap (WBG) semiconductor</b>	A semiconductor material with a significantly wider bandgap than conventional semiconductors such as silicon. Generally, a semiconductor is considered WBG if its bandgap is larger than 3 eV; examples include silicon carbide (3.3 eV) and gallium nitride (3.4 eV). WBG materials can enable device operation at higher frequencies, temperatures, and voltages compared to conventional semiconductors.