

## Project overview

### Project objectives

- Eliminate Surface Plasmon Polariton Losses in OLED
- Highly efficient single color PHOLED with >70% outcoupling efficiency
- Implement to white PHOLED with high EQE, CRI

Y1 goal for single color PHOLED

Y2 goal for white PHOLED

Y1 goal for single color PHOLED		Y2 goal for white PHOLED	
	Target	Current	Target
SPP Mode Coupling	50% Reduction	EQE [%]	70(210)*
		CRI	>90
		Size [cm <sup>2</sup> ]	23

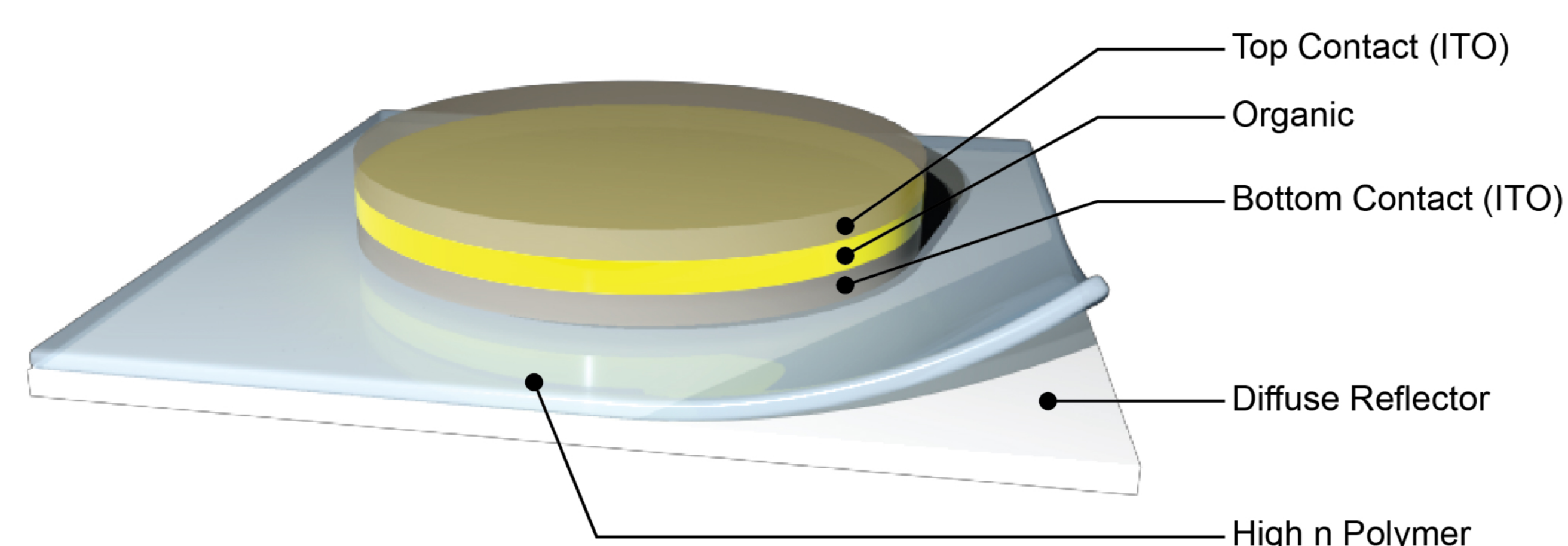
\* single EML assumed (stack WOLED)

Task	Task title	Target Q	Status
<b>1</b>	<b>High efficiency for single color PHOLED</b>		
1.1	Optical modeling for PHOLEDs	1	Complete
1.2	PHOLED with corrugation of cathode surface	3	Complete
1.3	Bottom-emitting OLEDs with optical gratings	4	Complete
1.4	Top-emitting OLEDs with a metallic scattering reflector	4	Complete
G/NG1	Single color PHOLED: Decrease in SPP mode coupling by 50%	4	Passed
<b>2</b>	<b>Highly efficient WOLED with high CRI</b>		
2.1	Develop WOLED with CRI>90	6	Complete
2.2	PHOLED with outcoupling >70%, WOLED with CRI >90 with 10% spectral shifts with angles	7	Complete
2.3	25 cm <sup>2</sup> packaged WOLEDs with outcoupling >70% and validation	8	With OLEDWorks

### Approach 1: Efficient Outcoupling via Light-Scattering Dielectric Layer

#### Concept

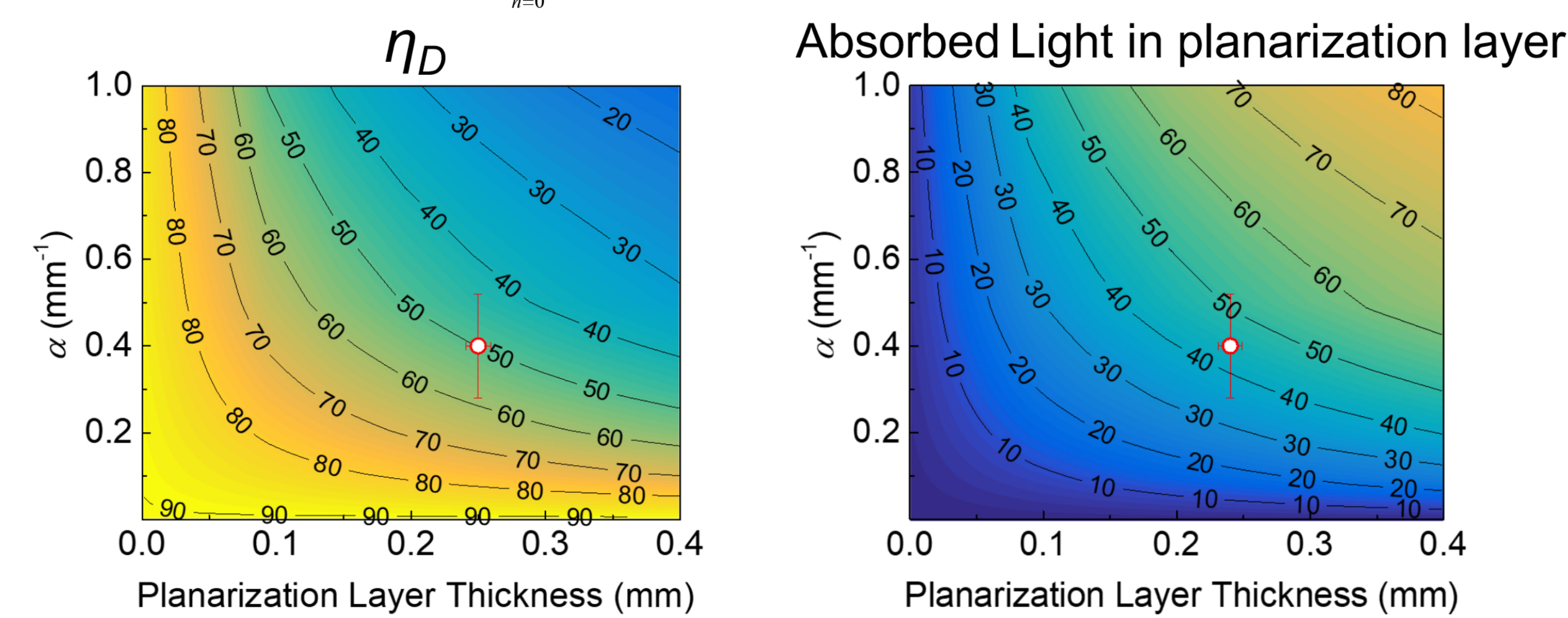
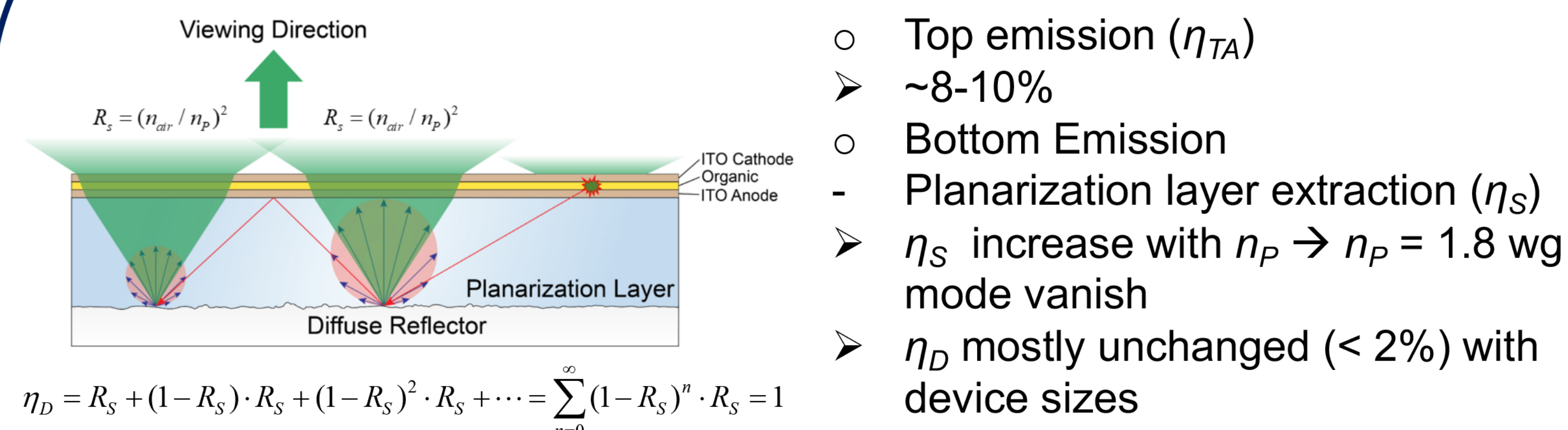
- Metal → ITO contact + Teflon (~95% R @ VIS)
- Cheap, Simple and Non-Intrusive
- Efficient, x 2.5 EQE by itself (x3.4 theory)
- No Angle/Wvl Dependence, Spectral Shift



Design Scheme

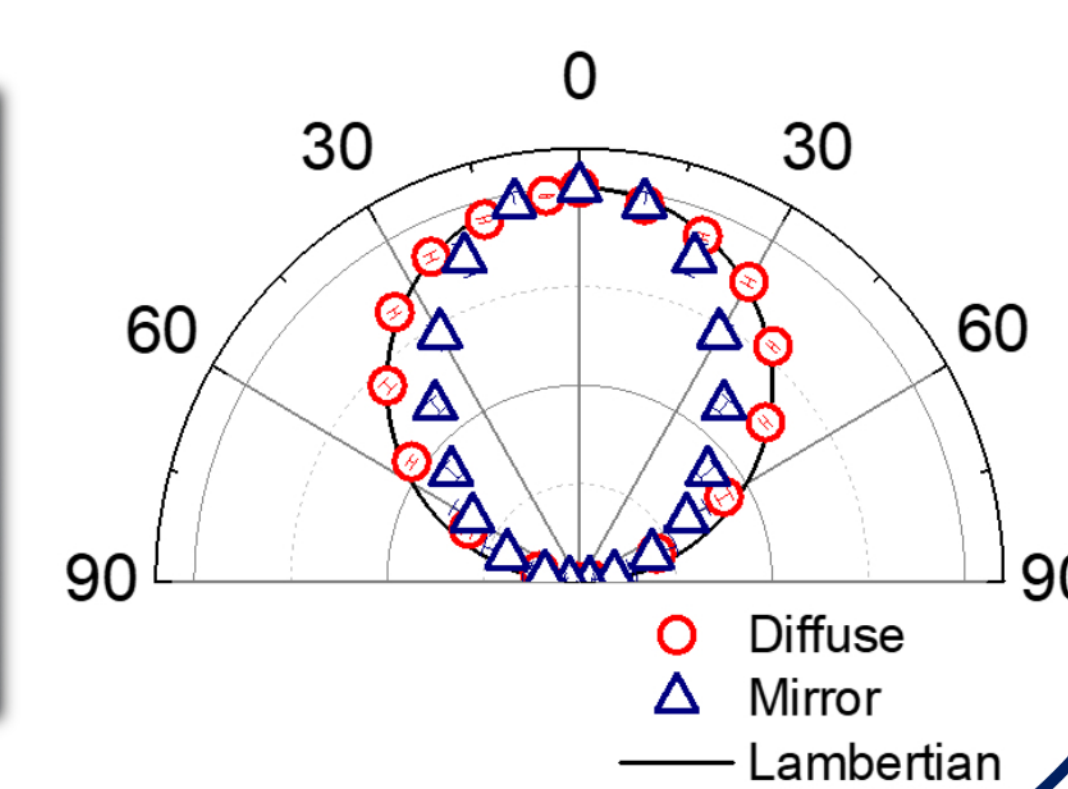
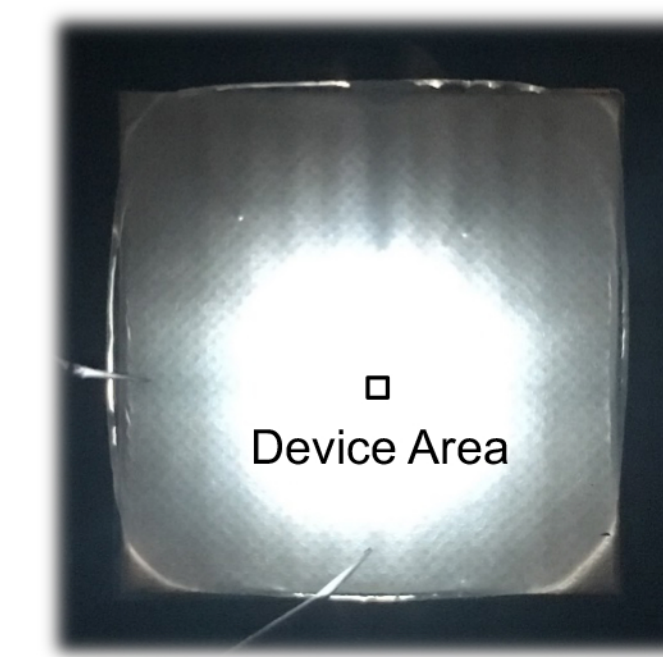
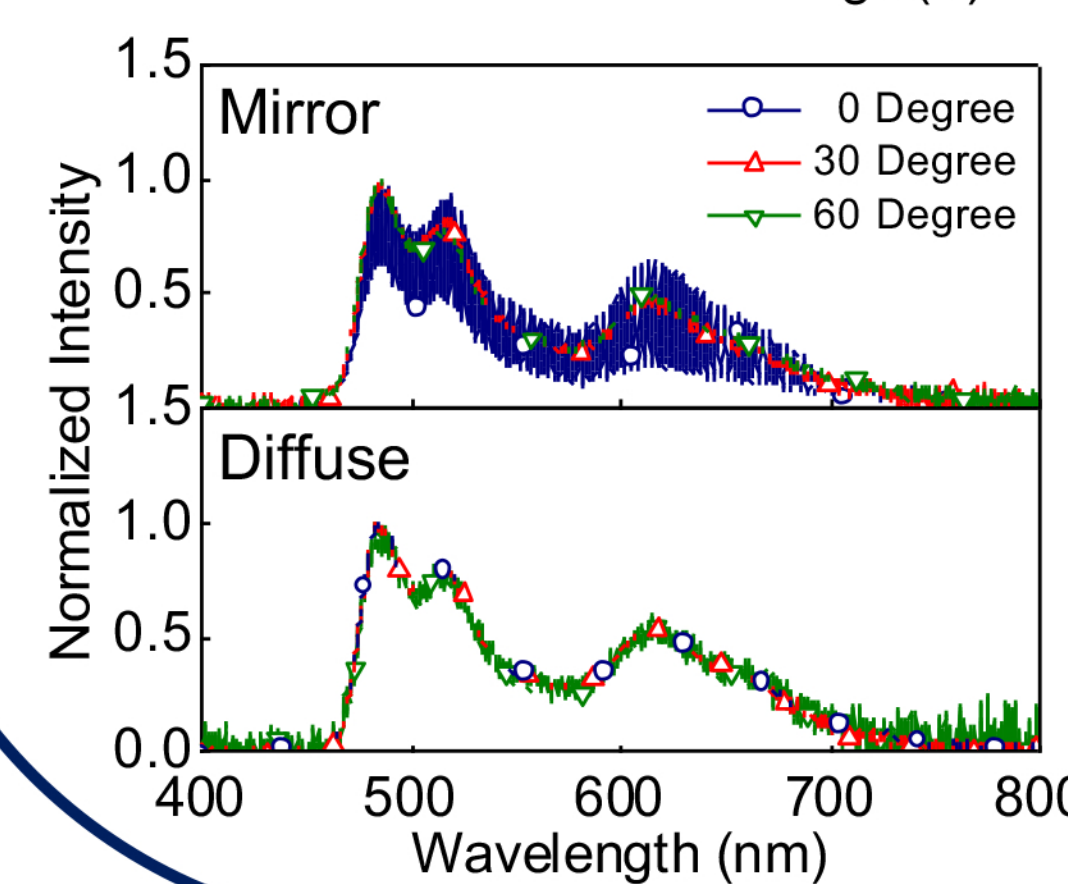
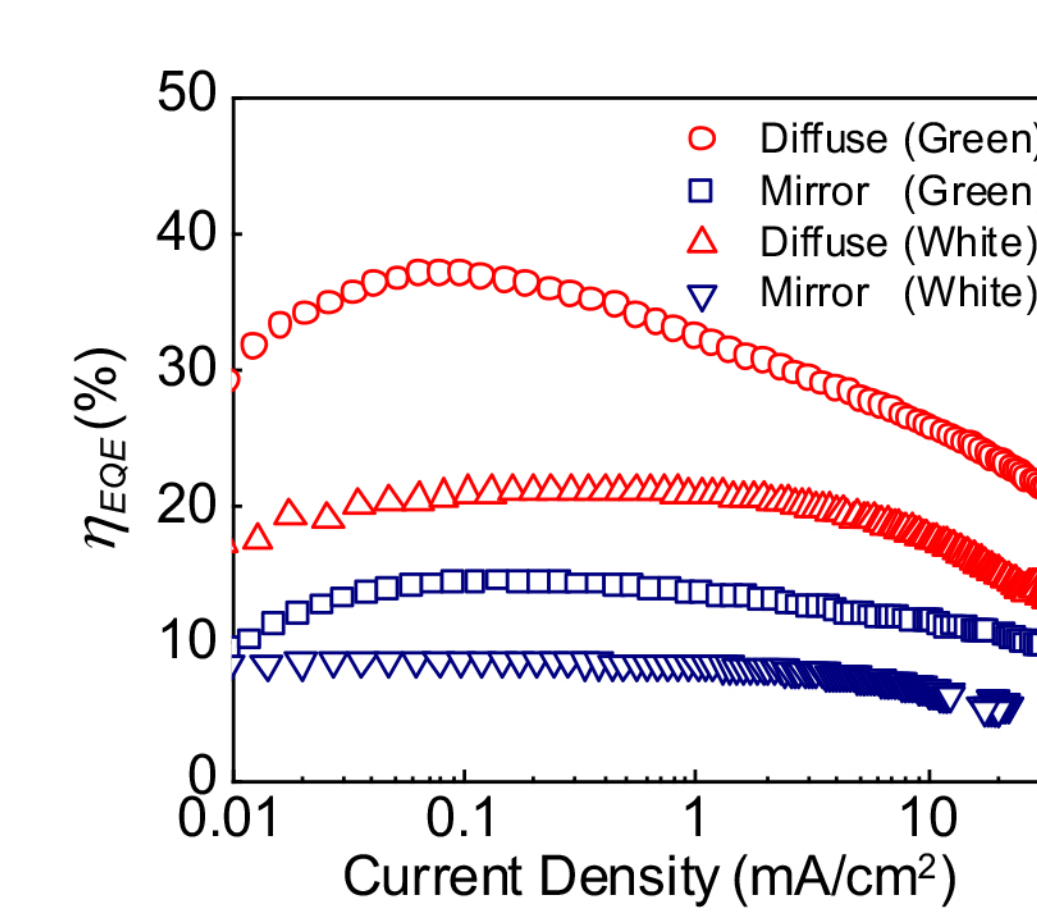
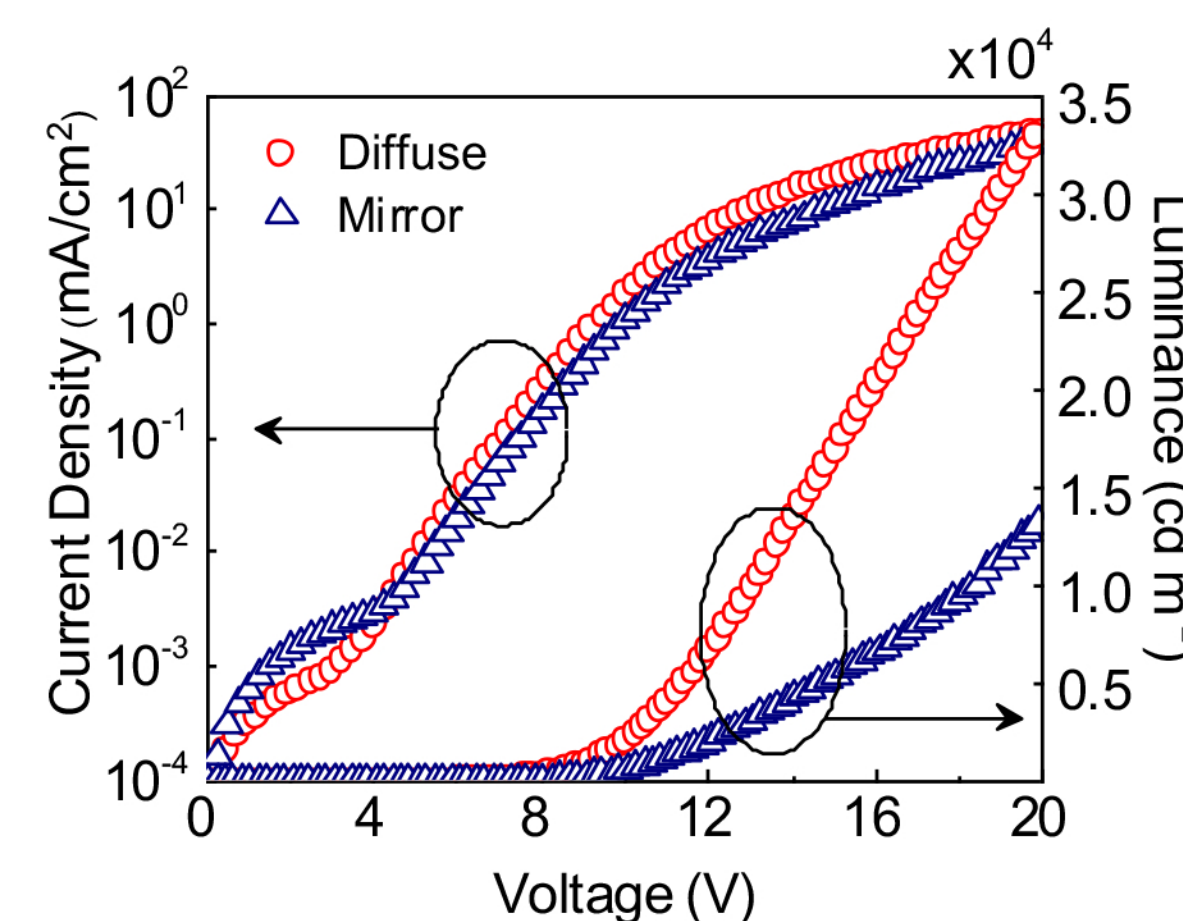
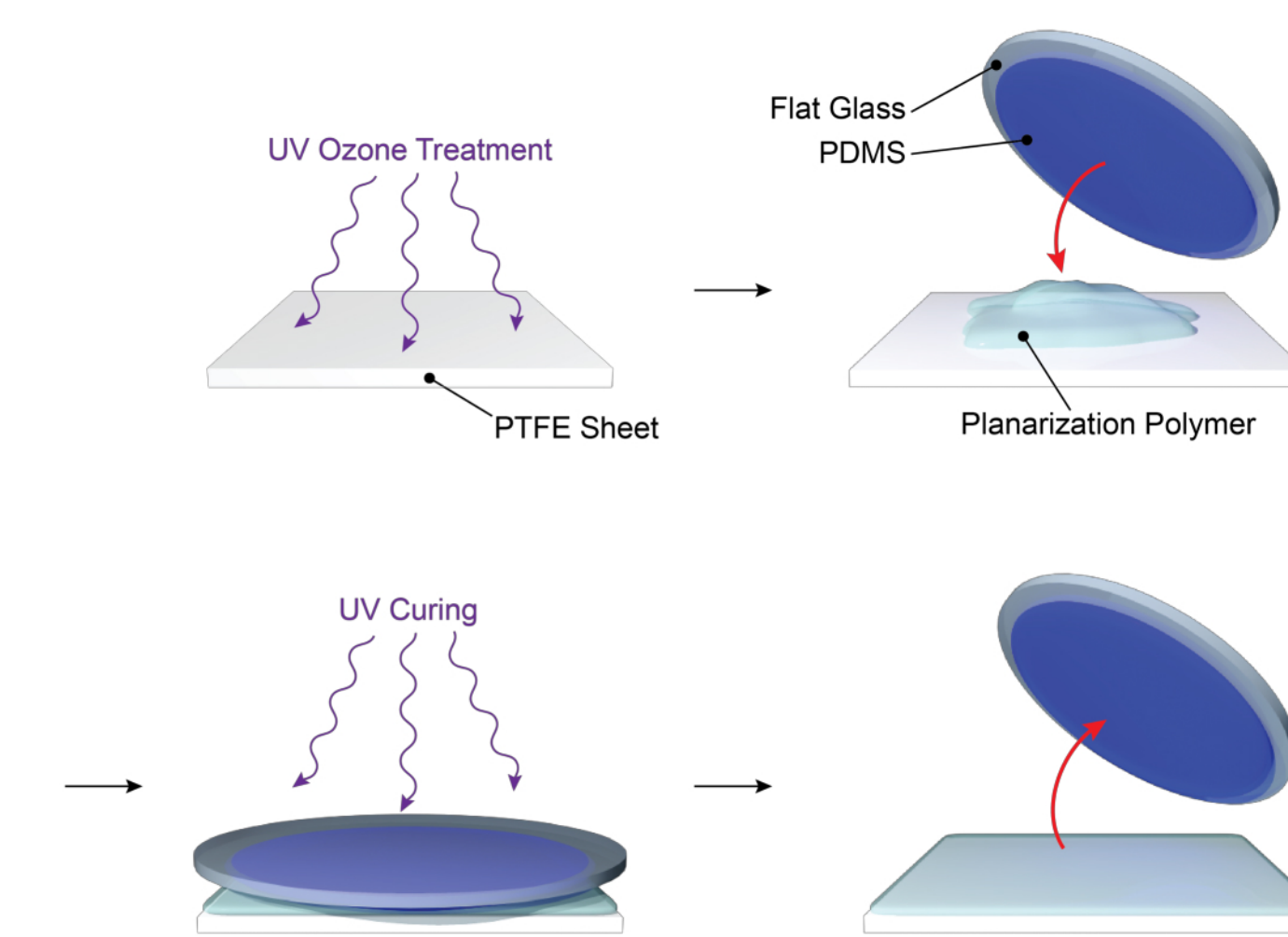
## R&D approach

### Device Structure and Fabrication Process



$$\eta_{out} = \eta_{TA} + \eta_D \eta_S$$

- Outcoupled light from planarization layer ( $\eta_D$ )
  - Thickness, absorption ↓ →  $\eta_D$  ↑
  - Major loss channel → planarization layer absorption

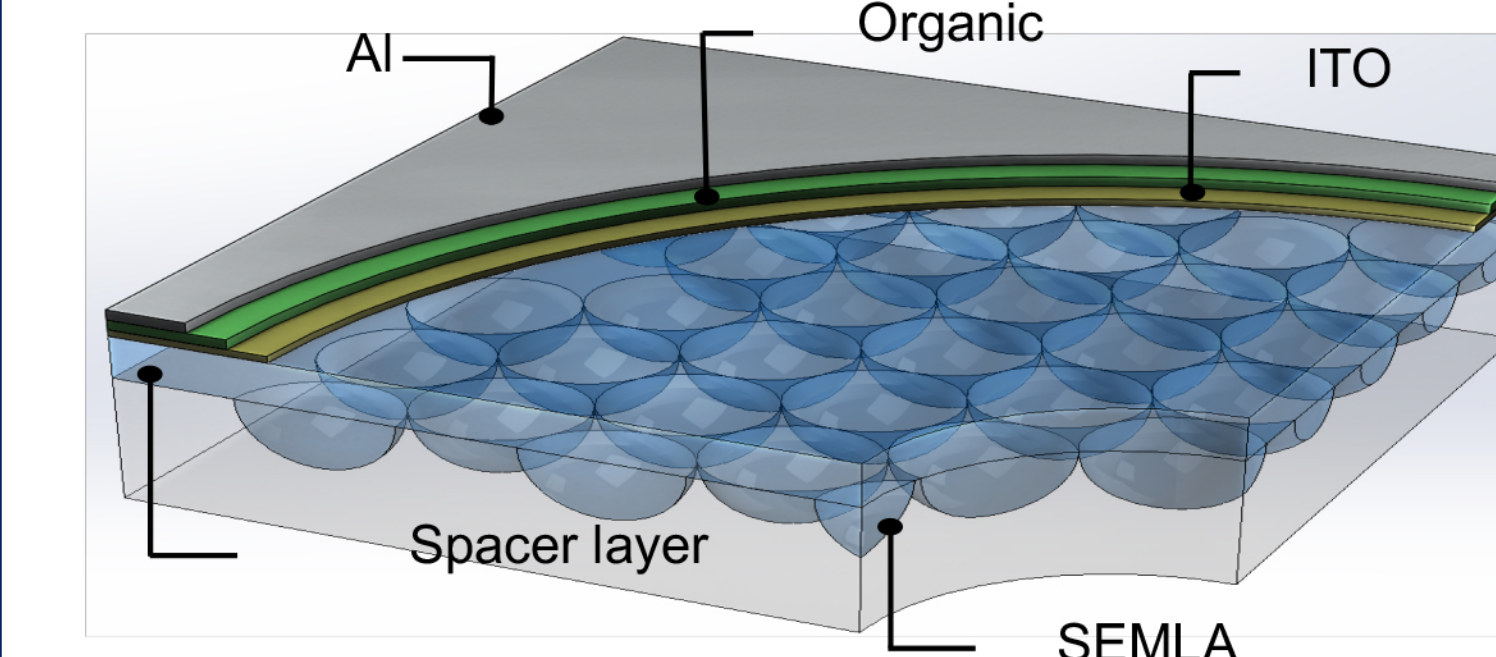


## R&D approach

### Approach 2: Efficient, Nonintrusive Outcoupling via Embedded Microlens Arrays

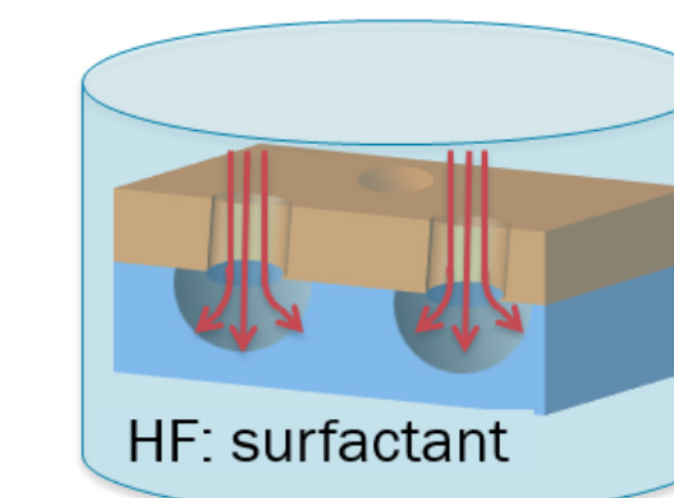
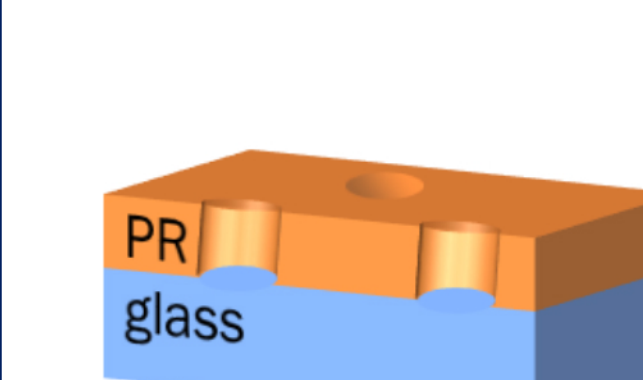
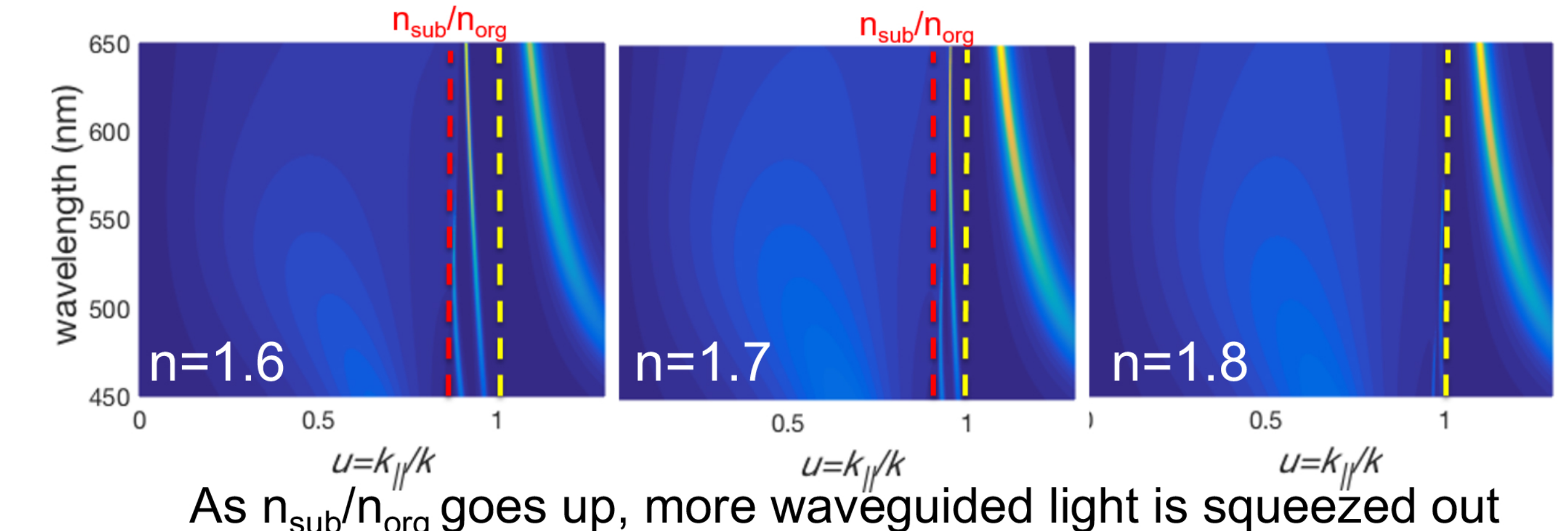
#### Concept

- Embed MLA into glass substrate (sub-Anode)
- Simple & Non-intrusive
- Efficient, EQE 70%
- No Angle/Wvl Dependence, Spectral Shift
- No Impact on Image Sharpness



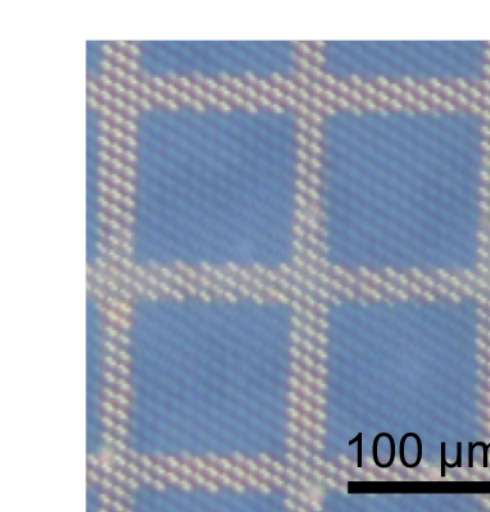
#### Design Scheme

- MLA between anode and glass
- Flat spacer layer
- High refractive index



#### Substrate Fabrication

- Lithography → Create holes
- Wet etch to produce MLA hemisphere



#### Image Resolution

- No apparent impact on image sharpness
- Good for display purposes

