## U.S. Department of Energy, Office of Electricity Transformer Resilience and Advanced Components Program Review

August 13-14, 2019

Oak Ridge National Laboratory, Hardin Valley Campus 2350 Cherahala Blvd, NTRC-3 Knoxville, TN 37932

## Day 1—Tuesday, August 13

TIME	AGENDA
7:30 – 8:00 am	Registration and Breakfast
8:00 – 8:10 am	Welcome and Introductions Rick Raines, Director, Electrical and Electronics Systems Research Division, Oak Ridge National Lab
8:10 – 8:20 am	Purpose, Agenda, Logistics Emmanuel Taylor, Energetics
8:20 – 8:50 am	Keynote Speaker Sanjay Bose, Senior Technical Advisor, U.S. Department of Energy
8:50 – 9:15 am	TRAC Program Overview Kerry Cheung, TRAC Program Manager, U.S. Department of Energy
9:15 – 9:30 am	BREAK
9:30 – 11:30 am	Group 1—Modeling and Analysis (30 min each)  > Suman Debnath, ORNL Models, Methods, & Tools to Analyze High-Penetration of Power Electronics in Grids; HVdc Models and Methods – Extension
	<ul> <li>Bjorn Vaagensmith, INL</li> <li>Evaluation of Grid Equipment Design Requirements for Improved Resilience</li> </ul>
	Zhi Li, ORNL Continuously Variable Series Reactor (CVSR) for Distribution System Applications; Tapless Regulating Power Transformer (TAREX)
	Paul Ohodnicki, NETL Development of Automated Design and Optimization Tools for High Frequency Magnetic Components and Migration to Open Source and High-Performance Computing Environments
11:30 am – 12:30 pm	LUNCH
12:30 – 2:30 pm	Group 2—Next-Gen Components 1 (30 min each)  Parag Upadhyay, ABB  Novel Concept for Flexible and Resilient Large Power Transformers
	<ul> <li>Ibrahima Ndiaye, GE Global Research</li> <li>Design, Deployment and Characterization of the World's First Flexible Large Power Transformer</li> </ul>
	Prasad Kandula, Georgia Tech Research Center Demonstration of a 5 MVA Modular Controllable Transformer (MCT) for a Resilient and Controllable Grid
	Alex Huang, UT Austin Modular Hybrid Solid State Transformer for Next Generation Flexible and Adaptable Large Power Transformer
2:30 – 2:45 pm	BREAK

2:45 – 4:45 pm	<ul> <li>Group 3—Next-Gen Components 2 (30 min each)</li> <li>➤ Madhu Chinthavali, ORNL         Solid State Power Substation (SSPS) Architecture Design</li> <li>➤ Subhashish Bhattacharya, North Carolina State University         Flexible Large Power Solid State Transformer</li> </ul>
	Sudip Mazumder, NextWatt Next-generation modular flexible low-cost silicon carbide (SiC) based high-frequency-link transformer
	Burak Ozpineci, ORNL Environmentally Neutral Automated Building Electric Energy (ENABLE) Platform
4:45 pm	Adjourn
6:00 pm	No Host Dinner: Calhoun's Oak Ridge, 100 Melton Lake Peninsula, Oak Ridge, TN

## Day 2—Wednesday, August 14

TIME	AGENDA
7:30 – 8:00 am	Registration and Breakfast
8:00 – 8:15 am	Overview of the Day, Logistics
8:15 – 10:15 am	Group 4—Sensing and Characterization (30 min each)
	<ul> <li>Sigifredo Gonzalez, SNL</li> <li>Advanced Sensors Field Validation (MagSense)</li> </ul>
	Timothy McIntyre, ORNL GMLC SAW Sensor Field Validation
	Paul Ohodnicki, NETL Optical Fiber Sensor Technology Development and Field Validation for Distribution Transformer and Other Grid Asset Health Monitoring
	Paul Ohodnicki, NETL Establishment of a Medium Voltage (MV) Core Loss Test System (CLTS) and Application Relevant Characterization of MV Dielectric and Insulation Materials
10:15 – 10:30 am	BREAK
10:30 am – 12:30 pm	Group 5—Materials and Manufacturing
	Alexander Plotkowski, ORNL (30 min) Microstructure Optimization of Electrical Steel Through Understanding Solidification Dynamics in Additive Manufacturing
	<ul> <li>Iver Anderson, Ames National Laboratory (30 min)</li> <li>Al/Ca Composite Conductor Characterization</li> </ul>
	Jesse Reeves, INL (20 min) Robust Insulation for Resilient Transformers and Power Electronics
	<ul> <li>Paul Ohodnicki, NETL (20 min)</li> <li>Soft Magnetic Alloy Advanced Manufacturing Through In-Line RF Processing;</li> <li>Metal / Oxide Nanocomposite Materials for High Frequency and High-Power Magnetics</li> </ul>
	<ul> <li>Jonathan Bock, SNL (20 min)</li> <li>Class II High Temperature Ceramic Capacitor Development</li> </ul>
12:30 – 1:30 pm	LUNCH
1:30 – 3:00 pm	Portfolio Discussion and Feedback
3:00 – 3:15 pm	BREAK
3:15 pm	Facility Tours (MDF, PE Lab)
4:45 pm	Adjourn