

# **Device, Converter and Controller Development for 10 MVA ETO Based STATCOM**

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- Future Renewable Electric Energy Delivery and Management (FREEDM) Systems Center is an NSF Engineering Research Center
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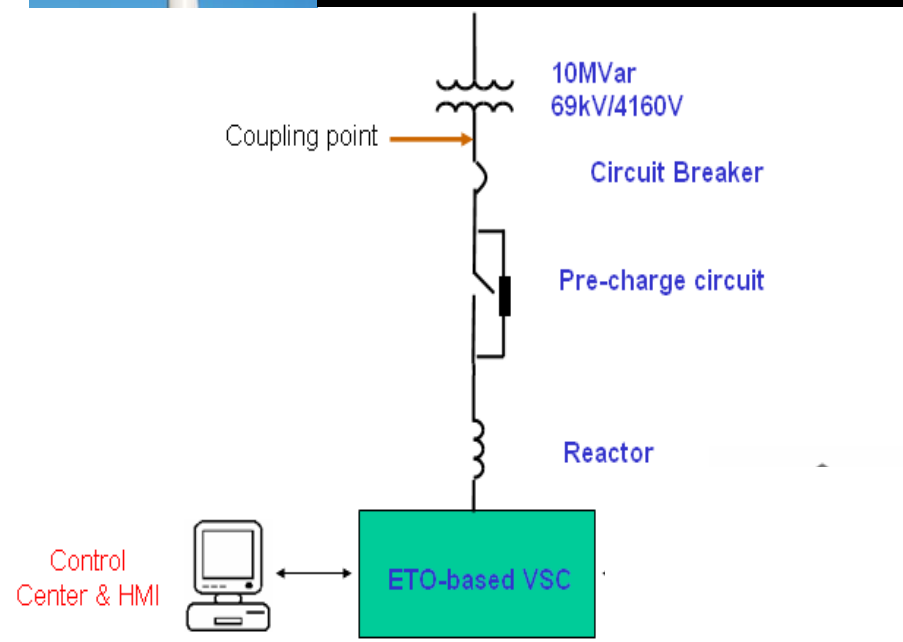
- **10 MVA ETO based STATCOM overview**
- **STATCOM converter topology**
- **Status of the devices and converter**
- **Controller architecture and test**
- **Summary**
- **Future work**

# Project Objective: Windfarm Integration

<b>Power rating</b>	<b>10 MVA</b>
<b>Transmission Line voltage</b>	<b>69 kV</b>
<b>STATCOM voltage</b>	<b>4.16 kV</b>
<b>Phase current</b>	<b>1.39 kA</b>
<b>Transformer</b>	<b>69kV/4.16kV</b>
<b>Converter</b>	<b>Cascade multilevel converter</b>
<b>Device</b>	<b>Gen-4 ETO (4.5kV/4kA )</b>



**Maximize power output by controlling voltage**



**10 MVA ETO-based STATCOM one line diagram**

## To demonstrate the following major benefits of ETO technology

### Low Cost

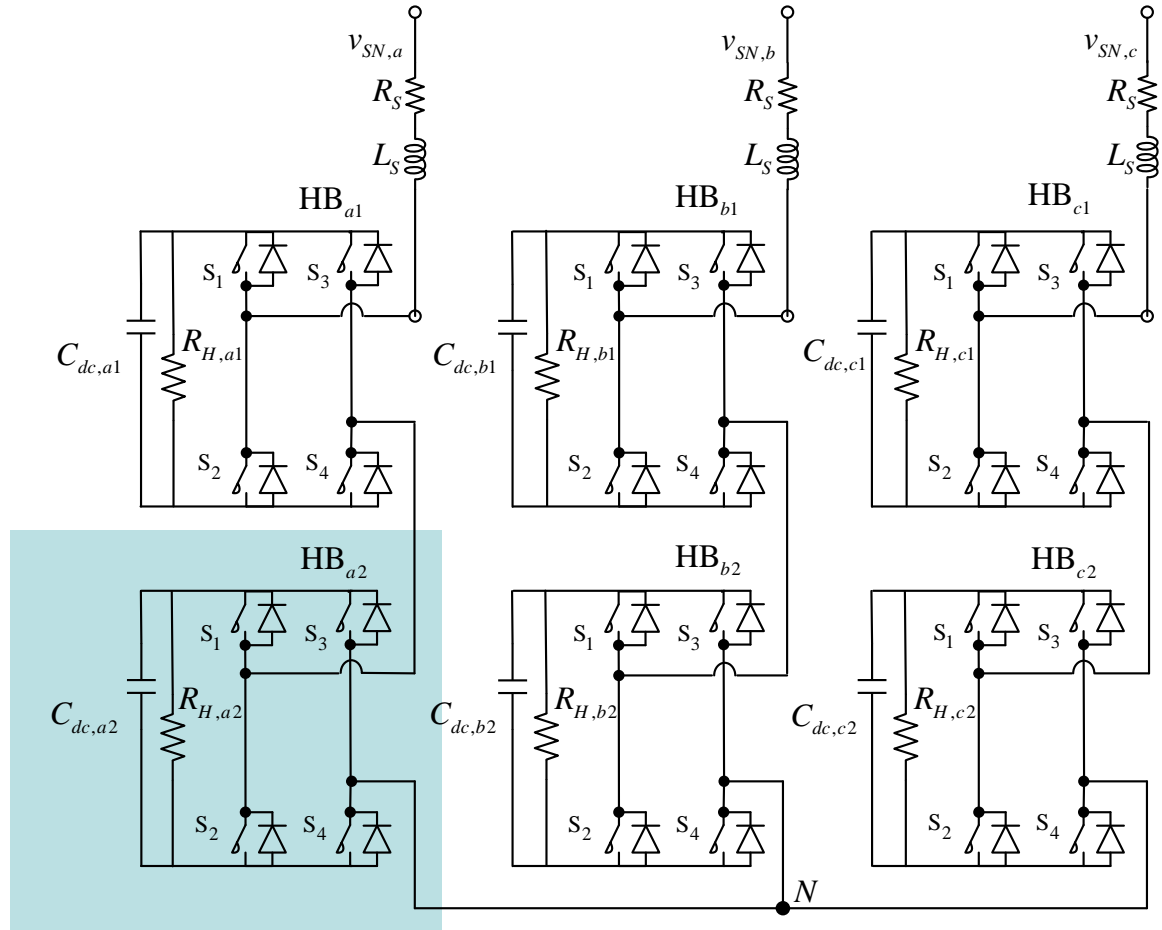
- Low-cost mega-watt rated power semiconductor device (ETO)
- Modular converter design (H-Bridge Building Block)
- Highly efficient air-cooled system

### High Reliability

- Large turn-off safe-operation area (SOA) of power device
- High performance cooling system
- Multi-layer protection

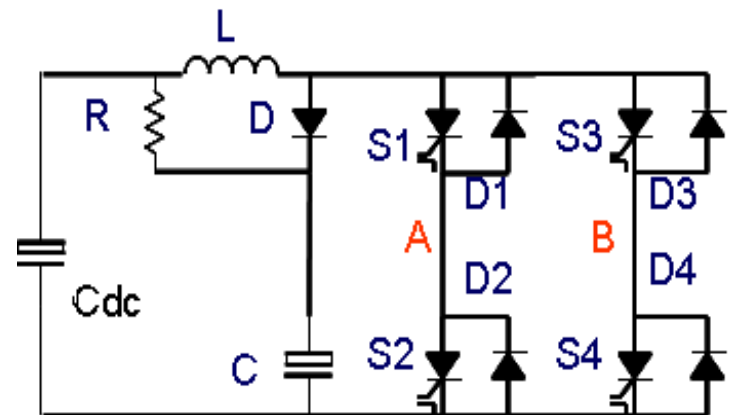
### Scalability and Extensionality

- Modular H-Bridge Building Block (HBBB)
- Scalable control system



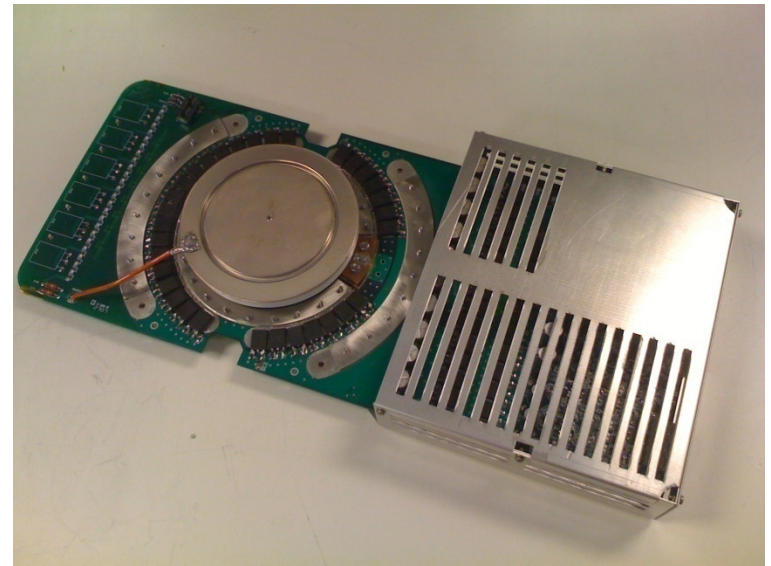
**Five-level cascade multilevel converter based on six modular H-Bridge Building Block (HBBB )**

<b>DC capacitor</b>	<b>2170 kV</b>
<b>DC capacitors value</b>	<b>12 mF</b>
<b>Device forward voltage</b>	<b>3.3 V</b>
<b>Diode forward voltage</b>	<b>3.1 V</b>
<b>Clamp L</b>	<b>5 <math>\mu</math>H</b>
<b>Clamp R</b>	<b>1 <math>\Omega</math></b>
<b>Clamp C</b>	<b>12 <math>\mu</math>F</b>



**Topology of the modular HBBB**

- ✓ **Switching Device Used in the HBBB is the Emitter Turn-Off Thyristor**
- ✓ **Hockey Puck Shaped, Press-pack device**
- ✓ **Self Powered Gate Drive**
- ✓ **Built-in Current, Voltage & Temperature Sensors**
- ✓ **Self-Protection capability**
- ✓ **Full Optical Control and Intelligent Feedback**
- ✓ **Clamping force required for correct operation**





	$P_{switching}$ (kW)	$P_{conduction}$ (kW)	<b>TOTAL</b> (kW)
S1	1099.5	1076	2175.5
S2	1099.5	1076	2175.5
S3	1099.5	1076	2175.5
S4	1099.5	1076	2175.5
D1	82.95	1049	1131.95
D2	82.95	1049	1131.95
D3	82.95	1049	1131.95
D4	<b>CAPACITIVE</b>	1131.95	1131.95

	$P_{switching}$ (kW)	$P_{conduction}$ (kW)	<b>TOTAL</b> (kW)
S1	740.31	1062	1802.31
S2	740.31	1062	1802.31
S3	740.31	1062	1802.31
S4	740.31	1062	1802.31
D1	116.07	1024	1140.07
D2	116.07	1024	1140.07
D3	116.07	1024	1140.07
D4	<b>INDUCTIVE</b>	1140.07	1140.07

**Total = 13 kW /per HBBS**

# LOSS SUMMARY of CLAMP CIRCUIT

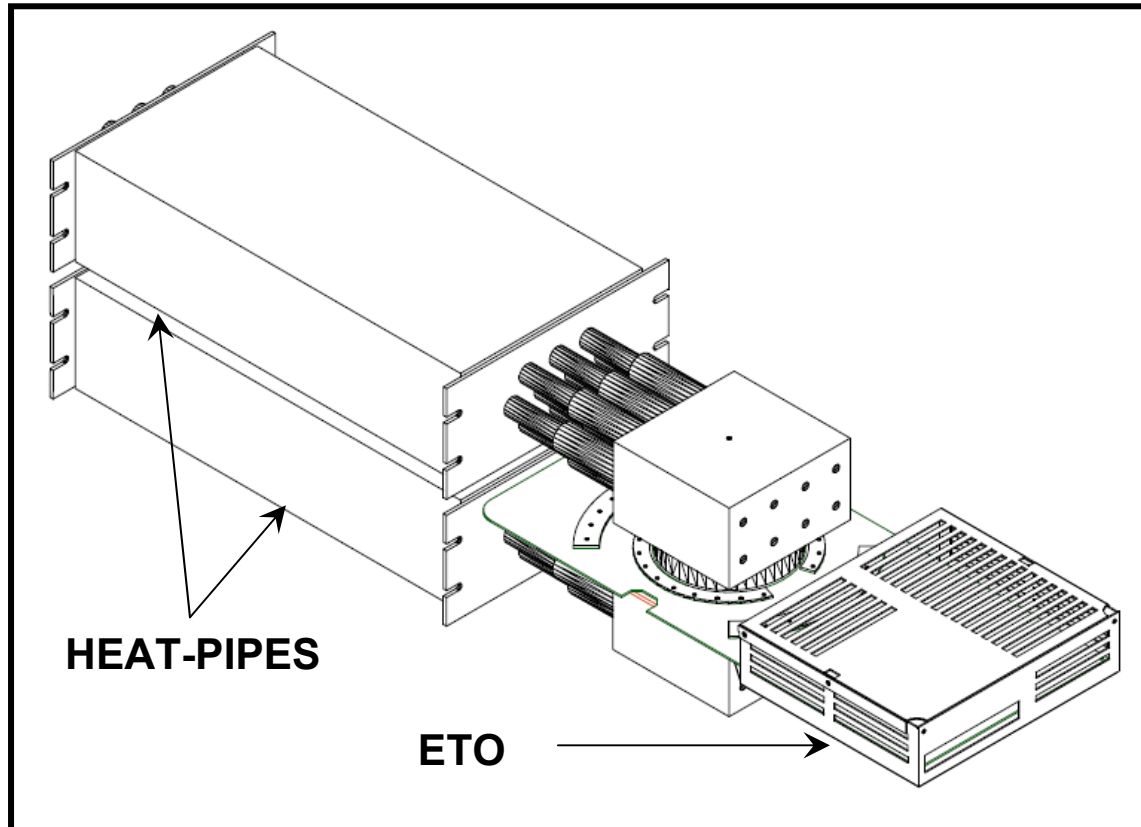
<b>Clamp Circuit Loss</b>	<b>5.32 kW</b> (resistor loss + diode loss)	<b>Resistor Loss</b>	<b>4.85 kW</b>	
		<b>Diode Total Loss : 472W</b>	<b>Forward recovery</b>	196 W
			<b>Conduction</b>	61W
			<b>Recovery</b>	216 W
<b>DC Capacitor (ESR Loss)</b>	382W			
<b>Di/dt Inductor</b>	Copper Loss (Resistance = 1.7 mΩ)		<b>663.63 W (CAP)</b>	<b>486.58 W (IND)</b>
<b>Maximum Clamp Capacitor Voltage @4000A</b>	3900 + 200 (ripple) = 4100V			
<b>Reset time @ 2000A</b>	16μs			

➤ **DESIGN CONSIDERATIONS:**

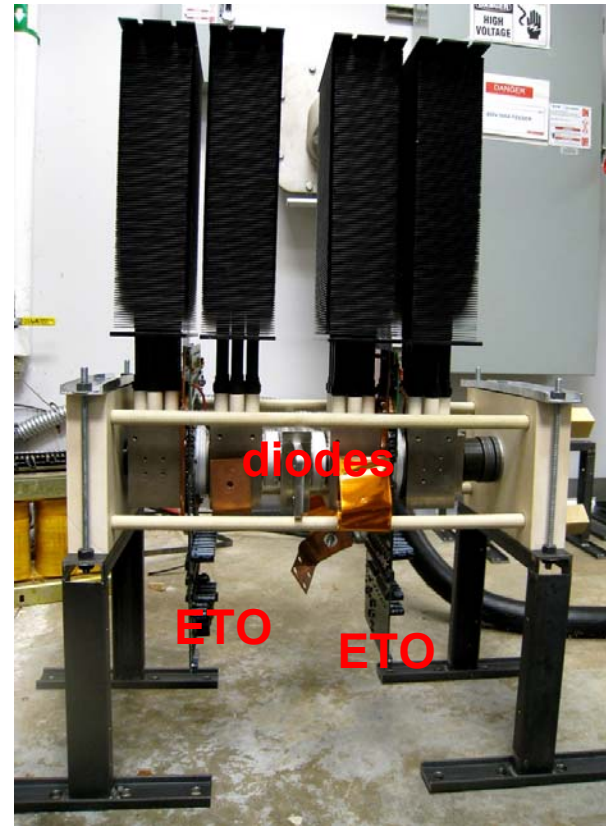
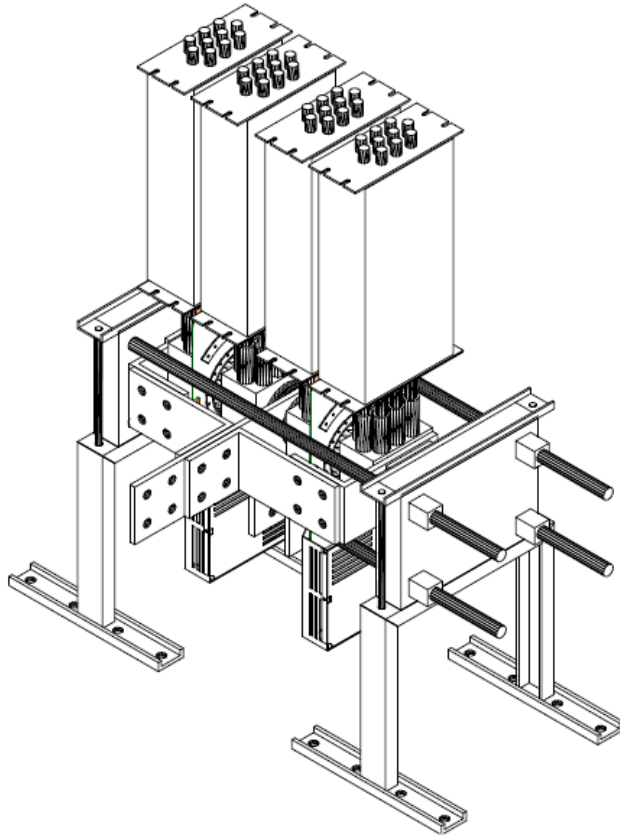
- ✓ ***Heat-Load is large and total HBBB loss = 20 kW***
- ✓ ***Largely Press-pack Devices are used in HBBB***
- ✓ ***Dog-House Resistors are used for the Clamp Resistor***
- ✓ ***Snubber Inductor has NO EXTERNAL FINS***
- ✓ ***SYSTEM HAS TO BE AIR-COOLED***

➤ ***Press-Pack Devices:***

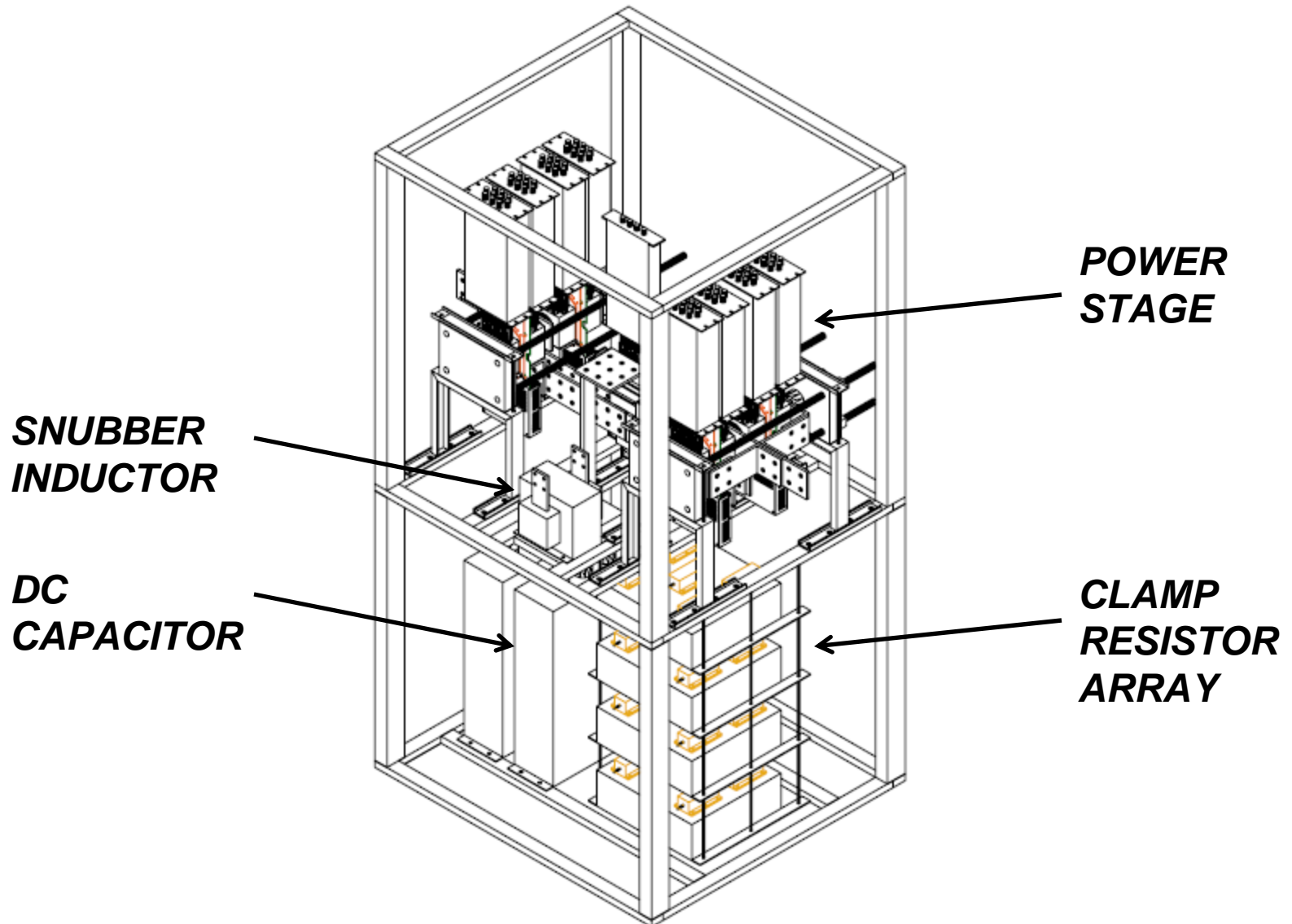
- ✓ ***Heat-Pipes are a good option for the ETO and Diode.***



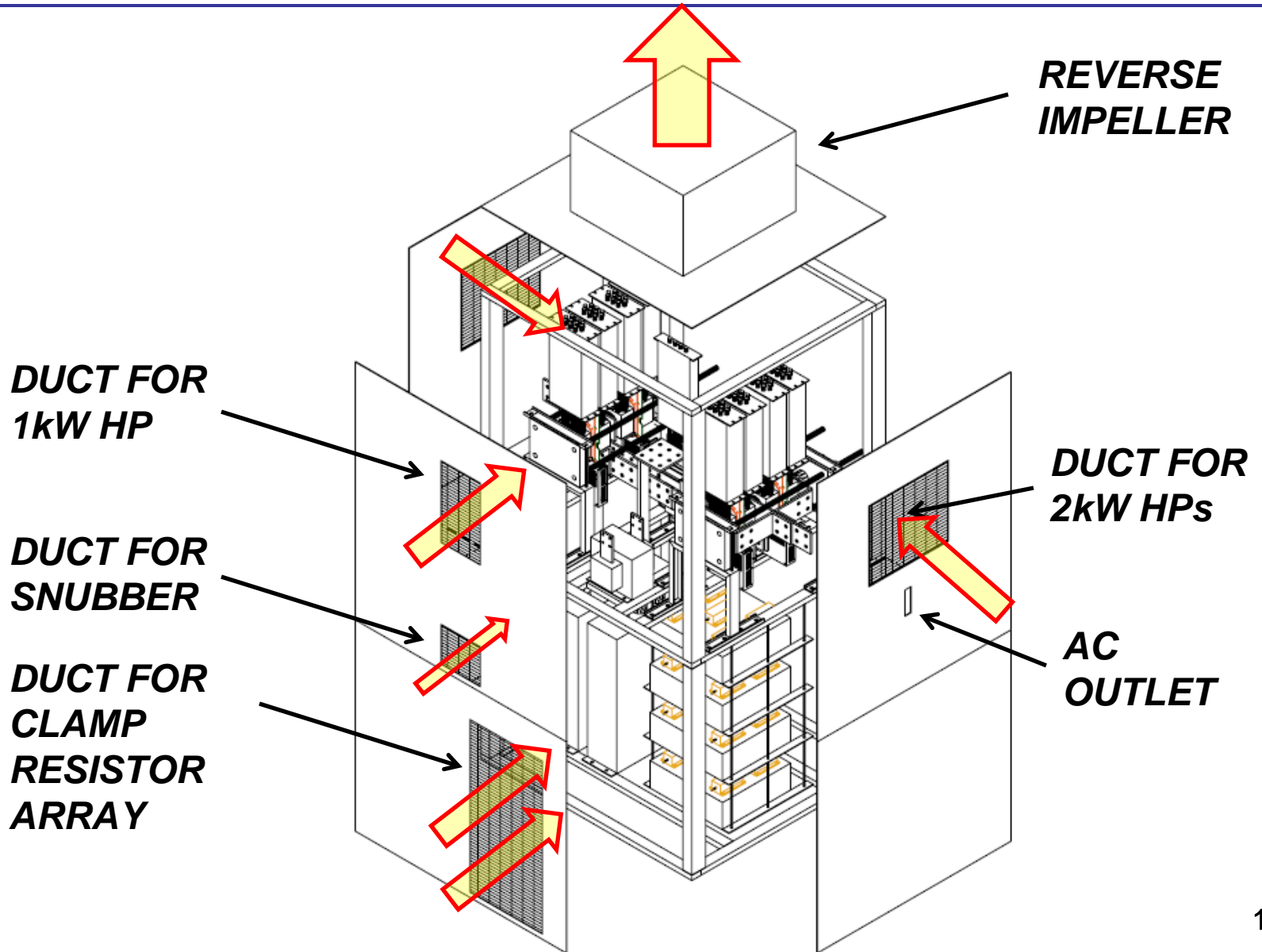
# ETO based Half-Bridge



# MECHANICAL DESIGN: HBFB Housing

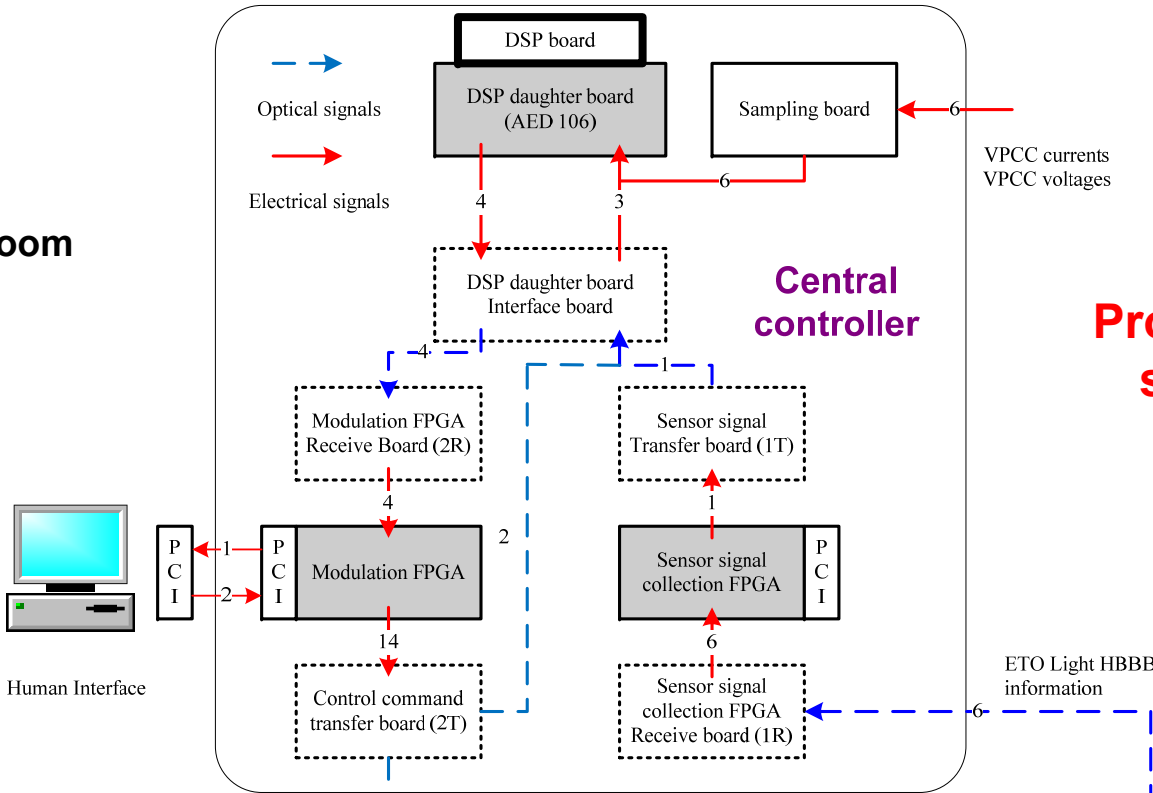


# AIR-FLOW DESIGN



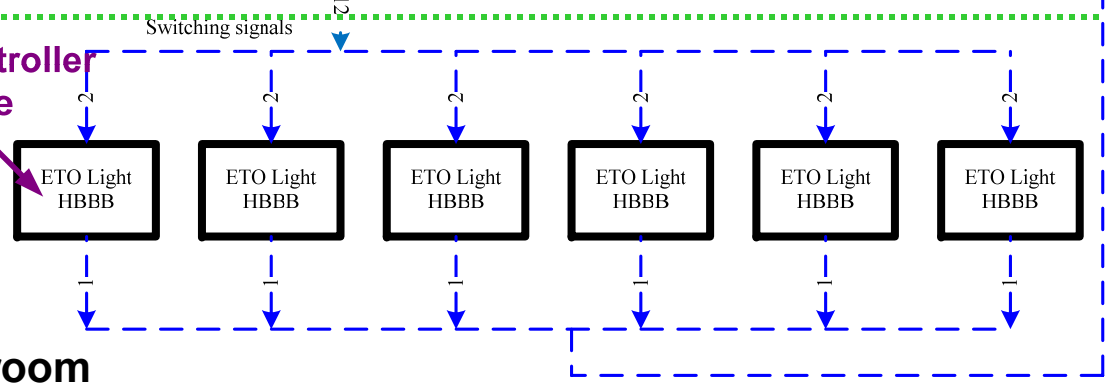
# STATCOM Controller Architecture

**Control room**



**Central controller:  
Proprietary OPWM control  
scheme developed for  
the STATCOM**

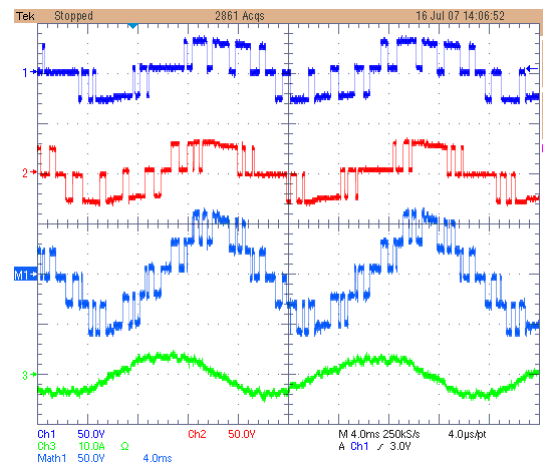
**Local controller  
inside**



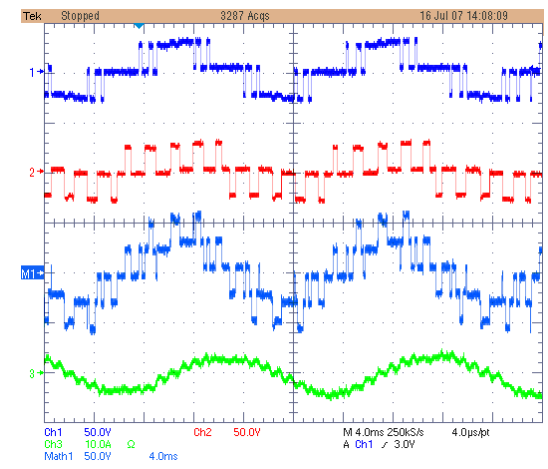
**Local controller:  
Implement intelligent  
over I, V, T protections**

**Converter room**

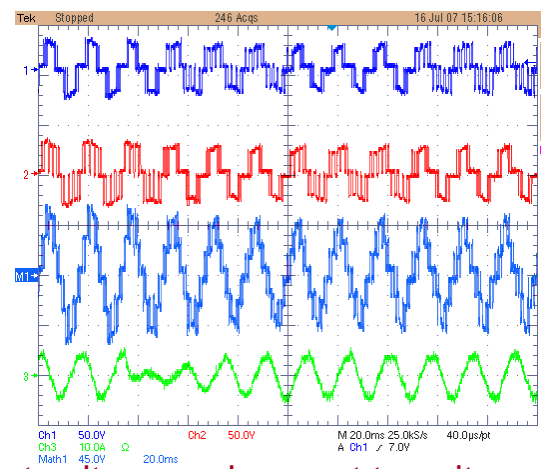




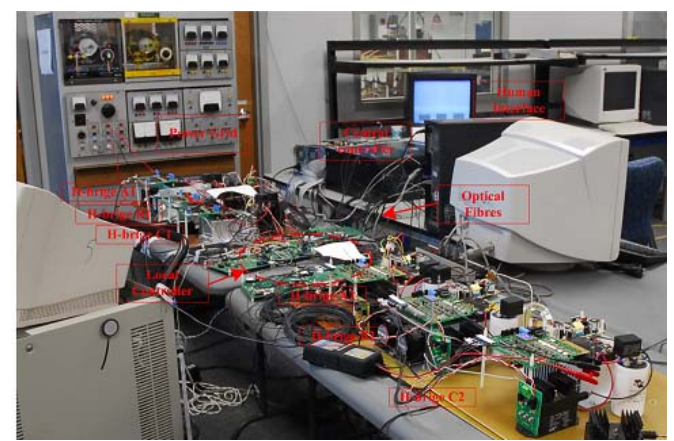
Output voltages of HBBBs and current in capacitive mode in the C phase



Output voltages of HBBBs and current in inductive mode in the C phase



Output voltage and current transit response during the transition from capacitive mode to inductive mode in the C phase



Controller setup in the laboratory (Six IGBT modules are built as six HBBBs)

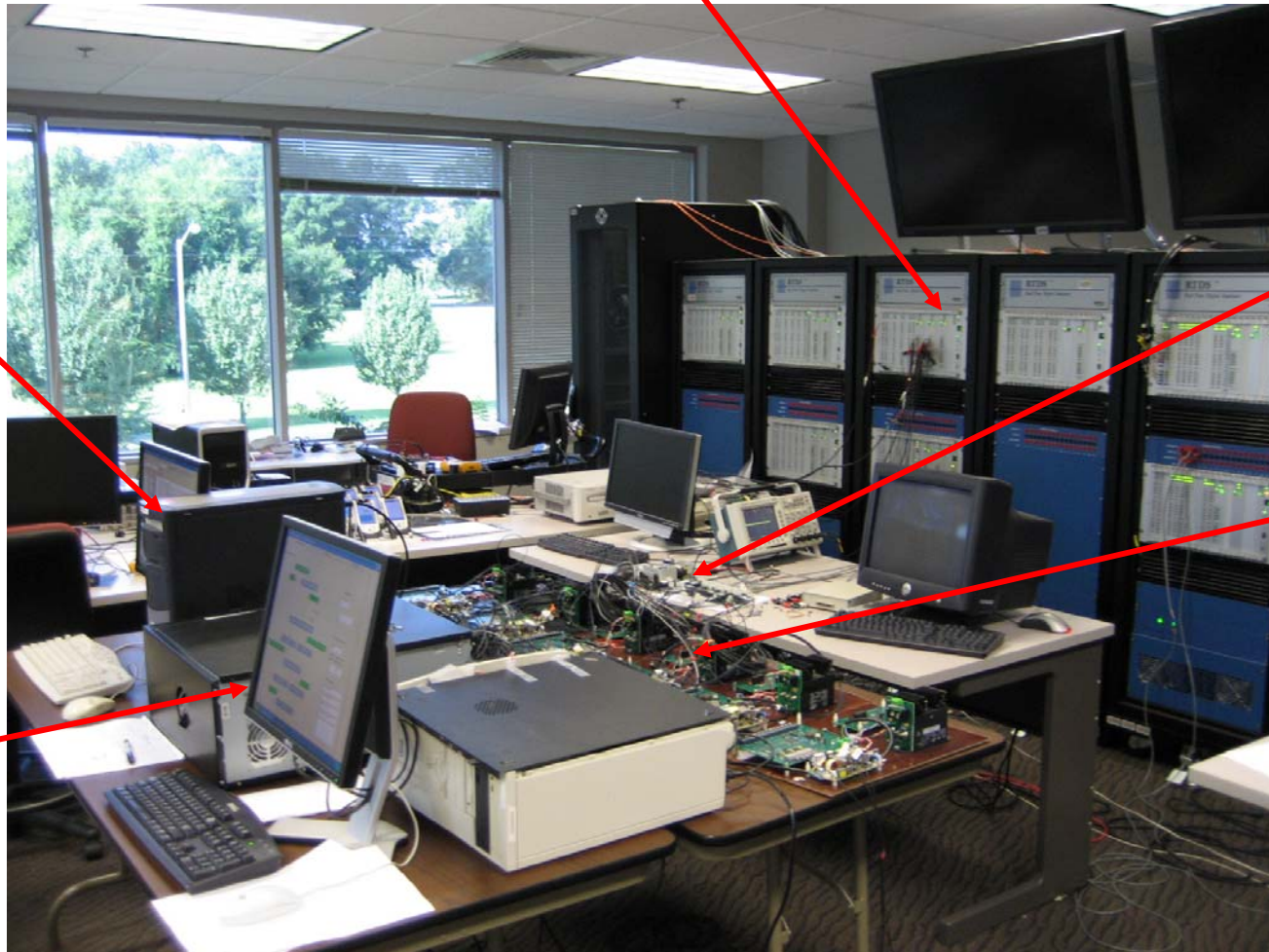
## RTDS (Real Time Digital Simulation) in FSU

**Central controller**

**Interface boards**

**Local controller**

**Human interface**



- **10 MVA STATCOM system power stage hardware are developed and ready**
  - 1) ETOs/diodes, Heatpipes, DC capacitors, clamp circuit for six HBBBs are ready
  - 2) Bus bar and mechanical design are finished.
  - 3) Bus bar manufacturing and assembly are needed to finish the 10 MVA system
- **STATCOM controller system is developed and is being tested in RTDS system supported**
- **Successful demonstration of ETO STATCOM will allow us to move to the next phase demonstration, an ETO Energy Storage System**

**THANK YOU**